

	NO.	ECO ND.	REVISIONS	DATE/BY
-	A		PRELIMINARY	6-12-96/LT

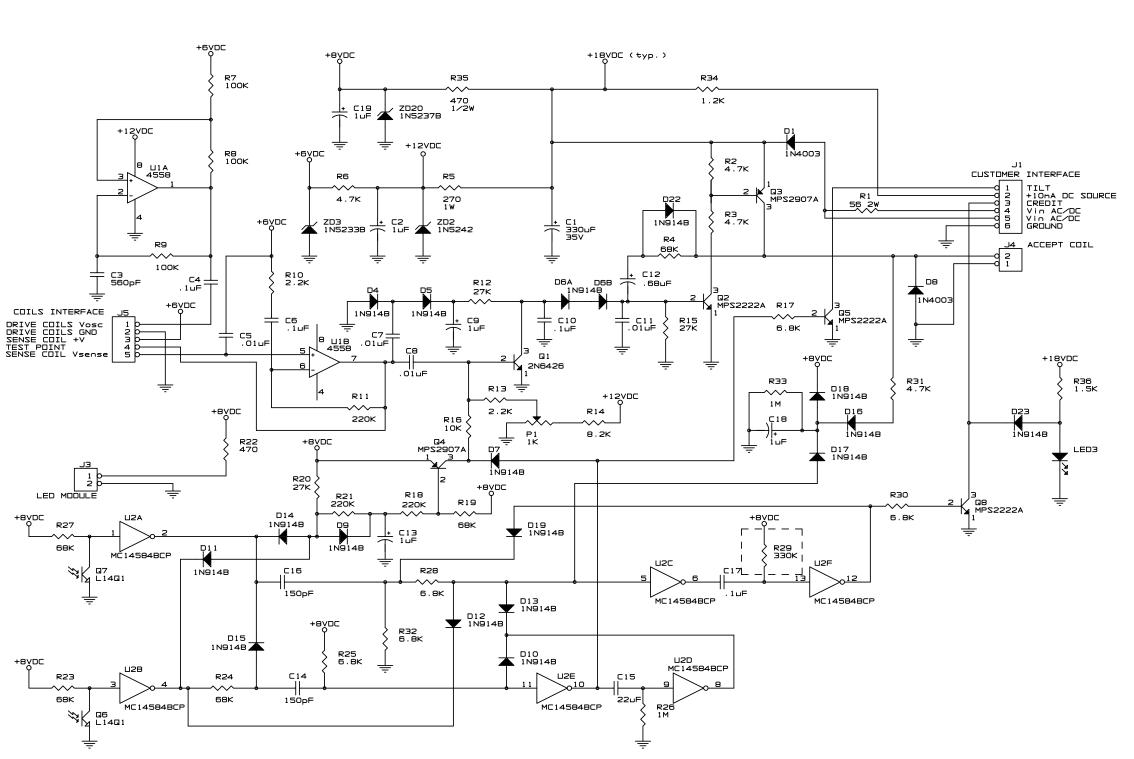
PRELIMINARY

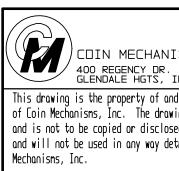
## NDTES:

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM.ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN.. UNLESS SPECIFIED OTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.
- 6. ATTACH A JUMPER WIRE FROM OLD LOCATION OF THE POSITIVE LEG OF C1 AND REATTACH TO THE POSITIVE LEG OF THE C1. (NEGATIVE LEG IS STILL CONNECTED TO SAME LOCATION)
  7. C1 MUST BE LAYED ON ITS SIDE FLUSH TO THE BOARD.
- P1 =SLUG REJECTION POTENTIOMETER

SCALE	2~1	DATE	_@	ดเพ				
DRAWN	22	5/3/94		oin <mark>Chani</mark>	-			
CHKD				CHANI	51	15	INC	,
TITLE PCB,C								
DWG. N	№.	0927	0053	SHEET	2	of	З	B

\_Tuesday, July 17, 2001 13:09:20\_





ECN ND.	REVISIONS	DATE/BY
$\times$	NEW RELEASE	5-14-92/KDH
003168	SEE ECO 003168	2-22-93/KDH
		8-22-94/SS
004072	TEXT ERROR, CHANGE U3 TO U2 ON SHEET 2	10-25-95/SS
004122	TEXT ERROR, CORRECTED COLOR ORDER OF J3 ON SHEET 2	2-8-96 LT
006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	6/27/00 BPP
	ND. 003168 003296 004072 004122	ND.       NEW       NEW       RELEASE         003168       SEE       ECD       003168         CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       CORRECT PARADDX BDM ERRORS FOR R10 & R13         003296       PCB,CTRL,CC-40,AC/DC,C25msT13s,NDI,SC,GAIN1         REMOVE LED3 P/N LL-2203R FROM SCHEMATIC.       004072         004072       TEXT ERROR, CHANGE U3 TO U2 ON SHEET 2         004122       TEXT ERROR, CORRECTED COLOR ORDER OF J3 ON SHEET 2         006150       ADDED D6B, CHANGE R14 FROM 20K TO 8.2K         006150       CHANGE Q1 FROM 2222 TO 2N6426,

02 006

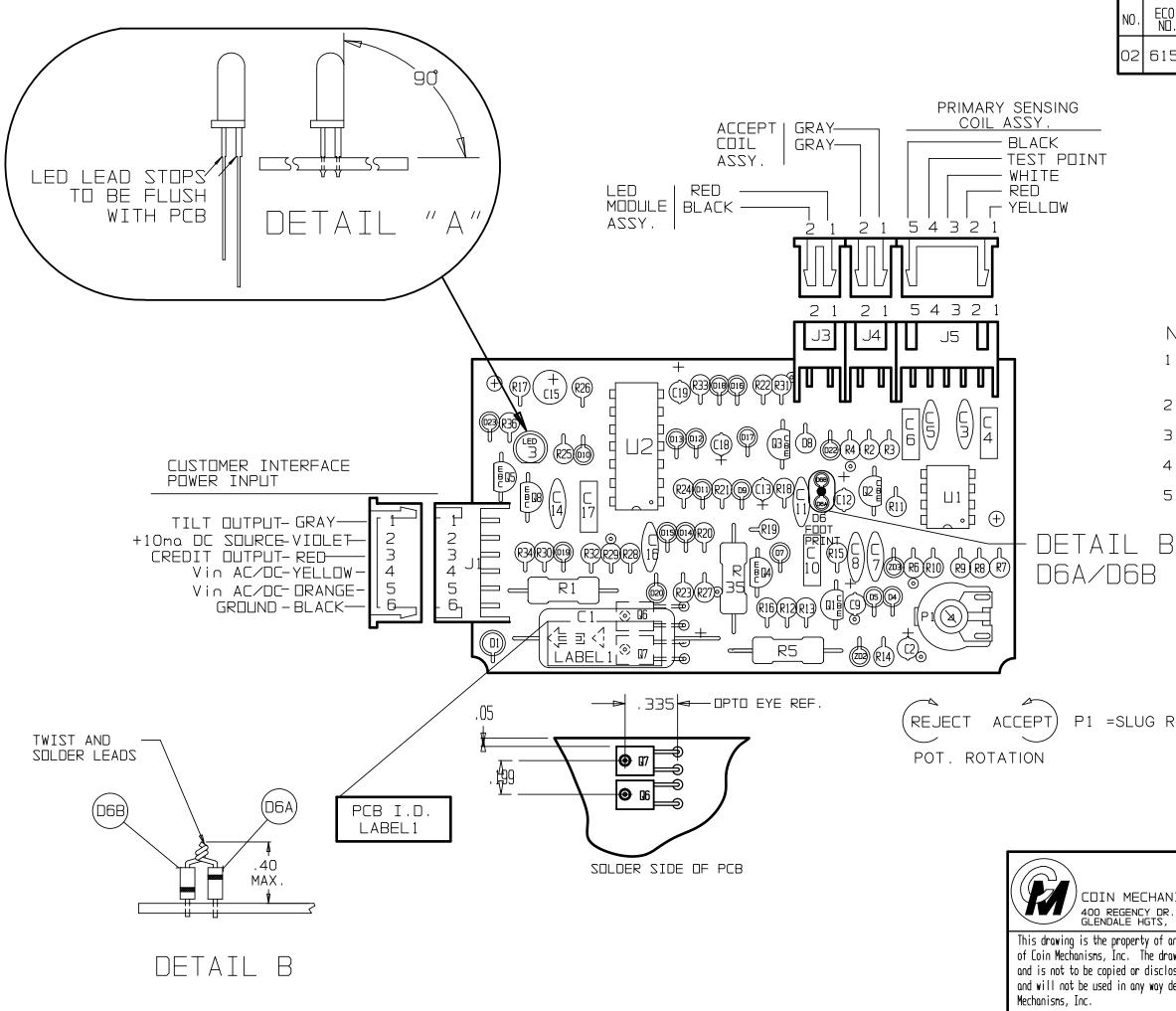
NO

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01

$\begin{split} & \square \top \vdash S : \\ 1 \cdot \square \text{ospecified capacitors are rated} \\ as follows: \\ \cdot 01 \square F = 25 \lor \\ \cdot 1 \square F = 63 \lor \\ \cdot 68 \square F = 35 \lor \\ 1 \square F = 35 \lor \\ 1 \square F = -35 \lor \\ 150 \square F = 500 \lor \\ 2 \cdot T \square T \square ULSE WIDTH = R26 * C15 \\ CREDIT PULSE WIDTH = R29 * C17 \\ 3 \cdot REMOVING D1, C1, ZD2, R5 AND \\ INSTALLING JUMPERS FOR D1 AND R5 \\ MAKES THIS A 12 \lor DC DPERATIONAL PCB. \end{split}$						
		TITLE				
			CTRL,CC			
ISMS INC,		C25MS,T13S,NDI,SC,GAIN1				
IL		DRAWN	KDH	DATE	5/14/	⁄92
nd contains proprietary infor		СНКД		DATE		
wing shall be returned upon r sed without written authoriza		APP		DATE		
etrinental to the interest of	DWG ND. 09270128				REV 2	
SIZEB SCA	ALE X=X	COMP N	D. XXXXXX	XX	SHT 1	OF 3

Tuesday, July 17, 2001 13:10:15



)	REVISIONS	DATE/BY
50	SEE SHEET 1	6/27/00 BPP

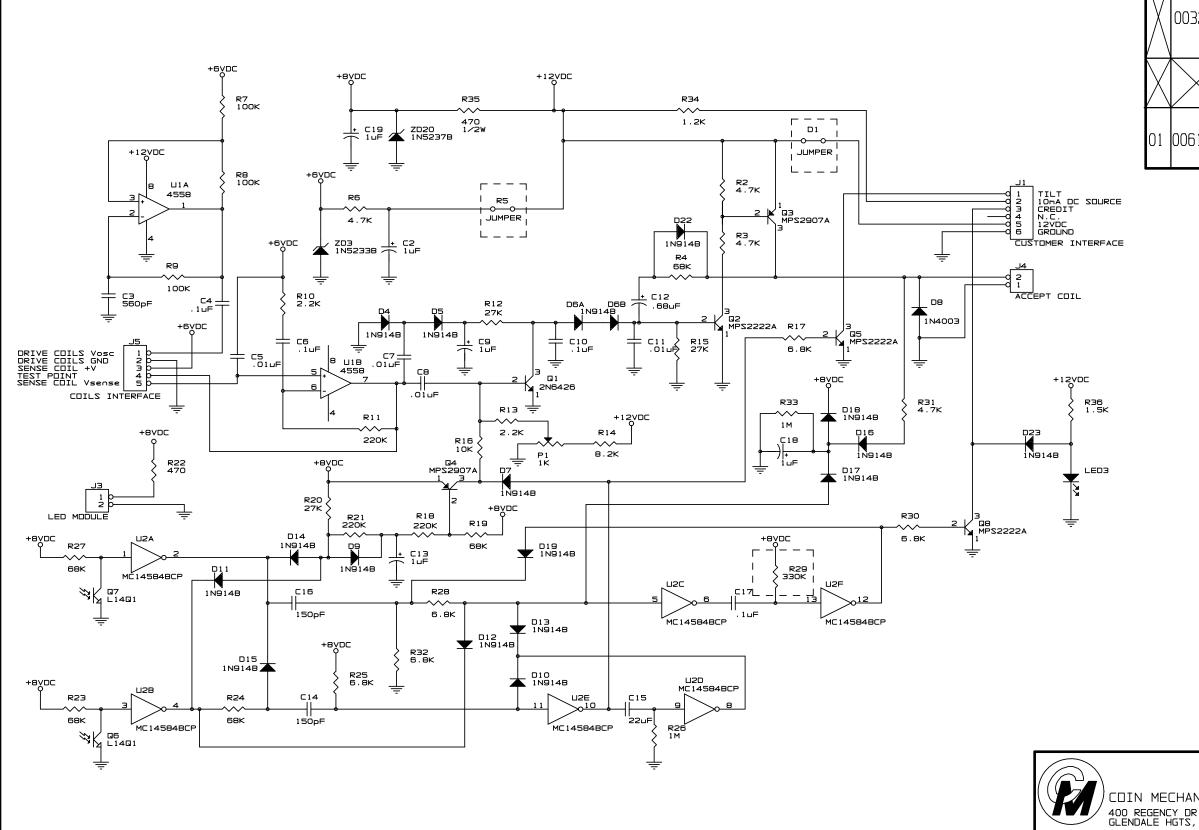
# NDTES:

- 1. REFERENCE DESIGNATIONS REFER TO REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM, ALSO SEE PARTS LIST FOR COMPONENT VALUES.
   MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN UNLESS SPECIFIED OTHERWISE.
   MAXIMUM COMPONENT LEAD PROJECTION TO

- BE .085 IN. 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815
- 5. ASSEMBLIES TO BE CLEAN OF FLUX AND OTHER OTHER CONTAMINANTS AFTER SOLDERING.

## P1 = SLUG REJECTION POTENTIOMETER

IISMS INC.			RL,CC-40,AC/DC, T13S,NDI,SC,GAIN1				
IL	DRAWN	КДН	DATE	5/14	/92		
nd contains proprietary information	СНКД		DATE				
wing shall be returned upon request used without written authorization	APP		DATE				
letrimental to the interest of Coin	DWG NE	<sup>),</sup> 09270	128		REV 2		
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1	0F 3		



This drawing is the property of a of Coin Mechanisms, Inc. The dra and is not to be copied or discla and will not be used in any way d Mechanisms, Inc.

\_Tuesday, July 17, 2001 13:11:16\_

•	ECN ND.	REVISIONS	DATE/BY
	$\times$	NEW RELEASE	5-14-92/KDH
	003168	SEE ECO 003168	2-22-93/KDH
/		CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TO PCB,CTRL,CC-40,AC/DC,C25msT13s,NDI,SC,GAIN1 REMOVE LED3 P/N LL-2203R FROM SCHEMATIC.	8-22-94/SS
7	$\times$	ERROR CORRECTION TO SHEET 2 OF 3 ADD R34, NEXT TO J1.	9-12-94/SS
	006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	6/27/00 BPP
-			

NO

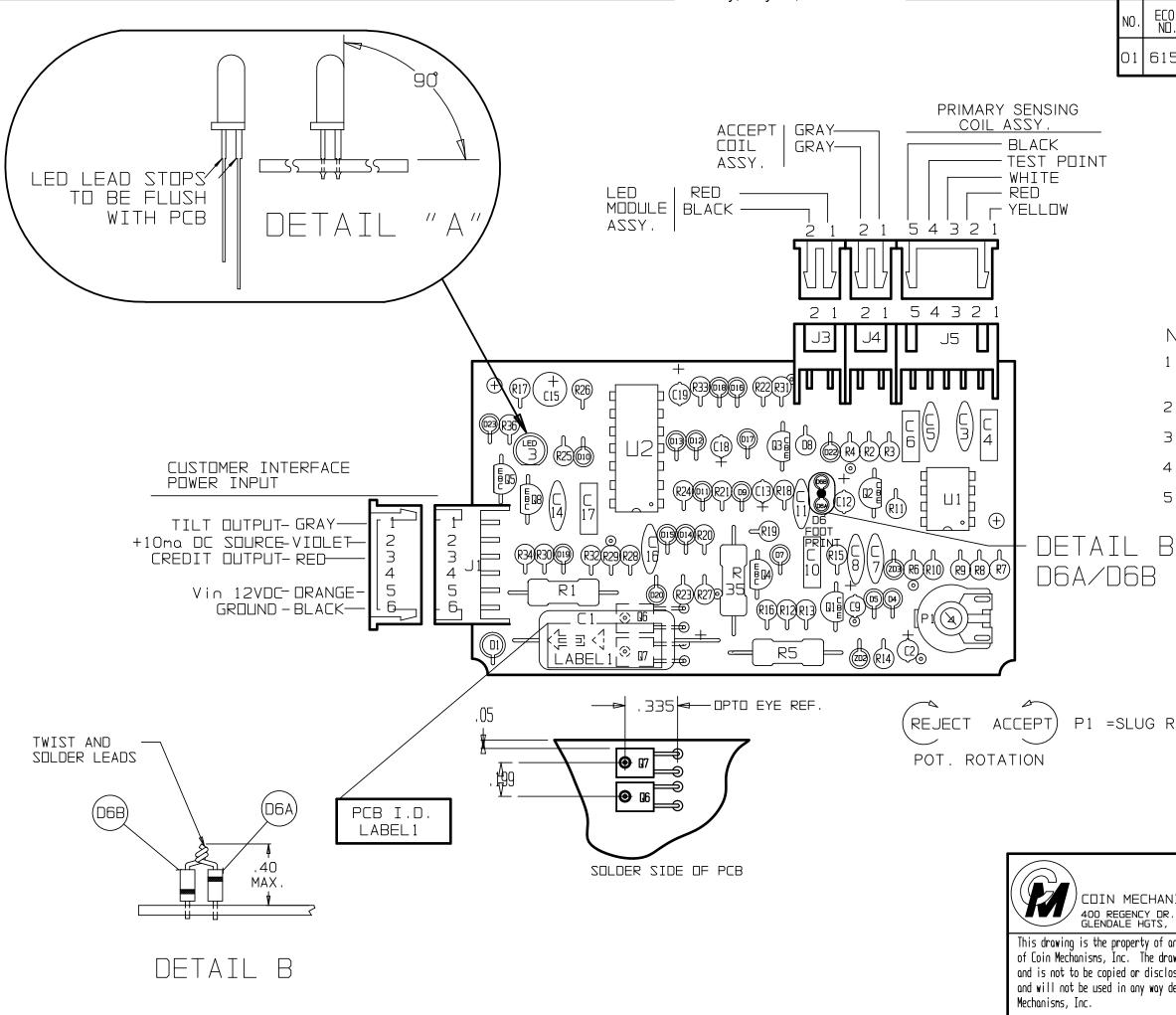
00

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as f .C .1 .E 1E 2TILT CREDI	ecifie ollows uF – iBuF – iGPF – PULSE TPULS	25V 63V 35V 35V 500V WIDTH = E WIDTH =	R26 * = R29 *	C15
		CTRL,CC S,T13S,		
	DRAWN	КОН	DATE	4/14/92

COIN MECHANISMS INC 400 REGENCY DR.		C25MS, T13S, NDI, SC, GAIN1				
GLENDALE HGTS, IL		DRAWN	КДН	DATE	4/14	/92
is the property of and contains pro		СНКД		DATE		
nisms, Inc. The drawing shall be returned upon request be copied or disclosed without written authorization				DATE		
be used in any way detrimental to		DWG NE	<sup>),</sup> 09270	129		<sup>rev</sup> 1
NC,	SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1	OF 3

Tuesday, July 17, 2001 13:12:01



)	REVISIONS	DATE/BY
50	SEE SHEET 1	6/27/00 BPP

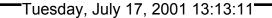
# NDTES:

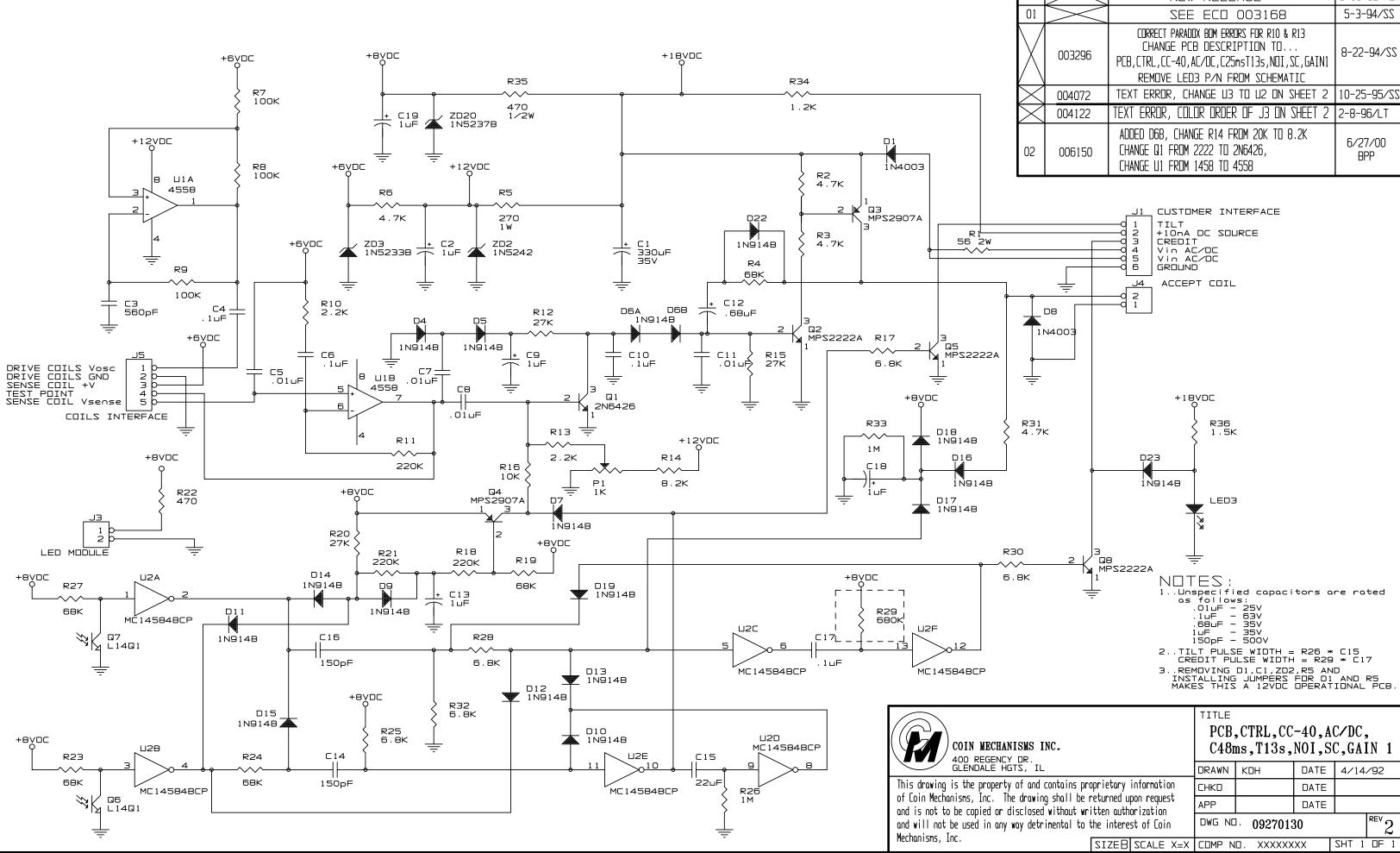
- 1. REFERENCE DESIGNATIONS REFER TO REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM, ALSO SEE PARTS LIST FOR COMPONENT VALUES.
   MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN UNLESS SPECIFIED OTHERWISE.
   MAXIMUM COMPONENT LEAD PROJECTION TO

- BE .085 IN. 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815
- 5. ASSEMBLIES TO BE CLEAN OF FLUX AND OTHER OTHER CONTAMINANTS AFTER SOLDERING.

## P1 =SLUG REJECTION POTENTIOMETER

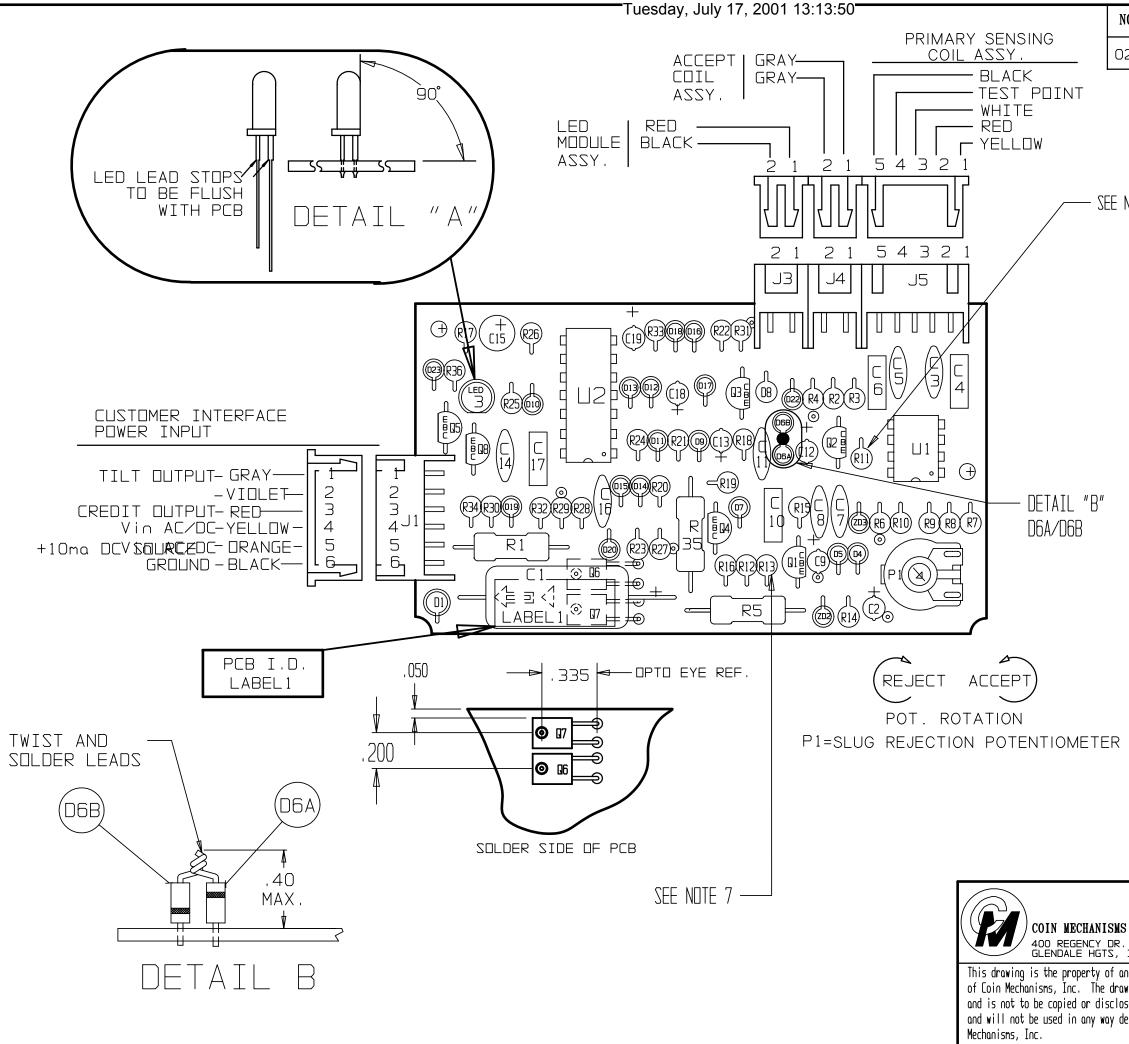
IISMS INC.		CTRL,CC S,T13S,			
IL	DRAWN	KDH	DATE	5/14	/92
nd contains proprietary information	СНКД		DATE		
wing shall be returned upon request used without written authorization	APP		DATE		
letrimental to the interest of Coin	DWG NE	<sup>I.</sup> 09270	129		<sup>rev</sup> 1
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1	0F 3





NO.	ECO NO.	REVISIONS	DATE⁄BY
00	$\ge$	NEW RELEASE	5-18-92/KDH
01	$\ge$	SEE ECO 003168	5-3-94/SS
$\left \right\rangle$	003296	CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TD PCB,CTRL,CC-40,AC/DC,C25msT13s,NDI,SC,GAIN1 REMOVE LED3 P/N FROM SCHEMATIC	8-22-94/SS
$\bowtie$	004072	TEXT ERROR, CHANGE U3 TO U2 ON SHEET 2	10-25-95/SS
$\bowtie$	004122	TEXT ERROR, COLOR ORDER OF J3 ON SHEET 2	2-8-96/LT
02	006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558	6/27/00 BPP

S INC.		TITLE PCB,CTRL,CC-40,AC/DC, C48ms,T13s,NOI,SC,GAIN 1				
IL			DRAWN	KDH	DATE	4/14/92
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		СНКД		DATE		
		APP		DATE		
		DWG ND. 09270130			REV 2	
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1



NO.	ECO NO.	REVISIONS	DATE⁄BY
22	6150	SEE SHEET 1	6/27/00/ BPP

SEE NOTE 6

NDTES:

1.	REFERENCE DESIGNATIONS REFER
	TO SCHEMATIC DIAGRAM, ALSO SEE
	PARTS LIST FOR COMPONENT VALUES

2.	MAXIMUM HEIGHT	ON ANY COMPONENT
		UNLESS SPECIFIED
	DTHERWISE.	

	OTHERWISE,		
З.	MAXIMUM COMPONENT	LEAD	PROJECTION

	TO BE .085 IN	
4.	SOLDERIN SHALL BE IN ACCORDACE	

4,	SOLDERIN	SHALL	ΒE	ΙN	ACCORDACE

WITH IPC-S-815 ASSEMBLIES TO BE CLEANED OF FLUX 5. AND OTHER CONTAMINANTS AFTER SOLDERING

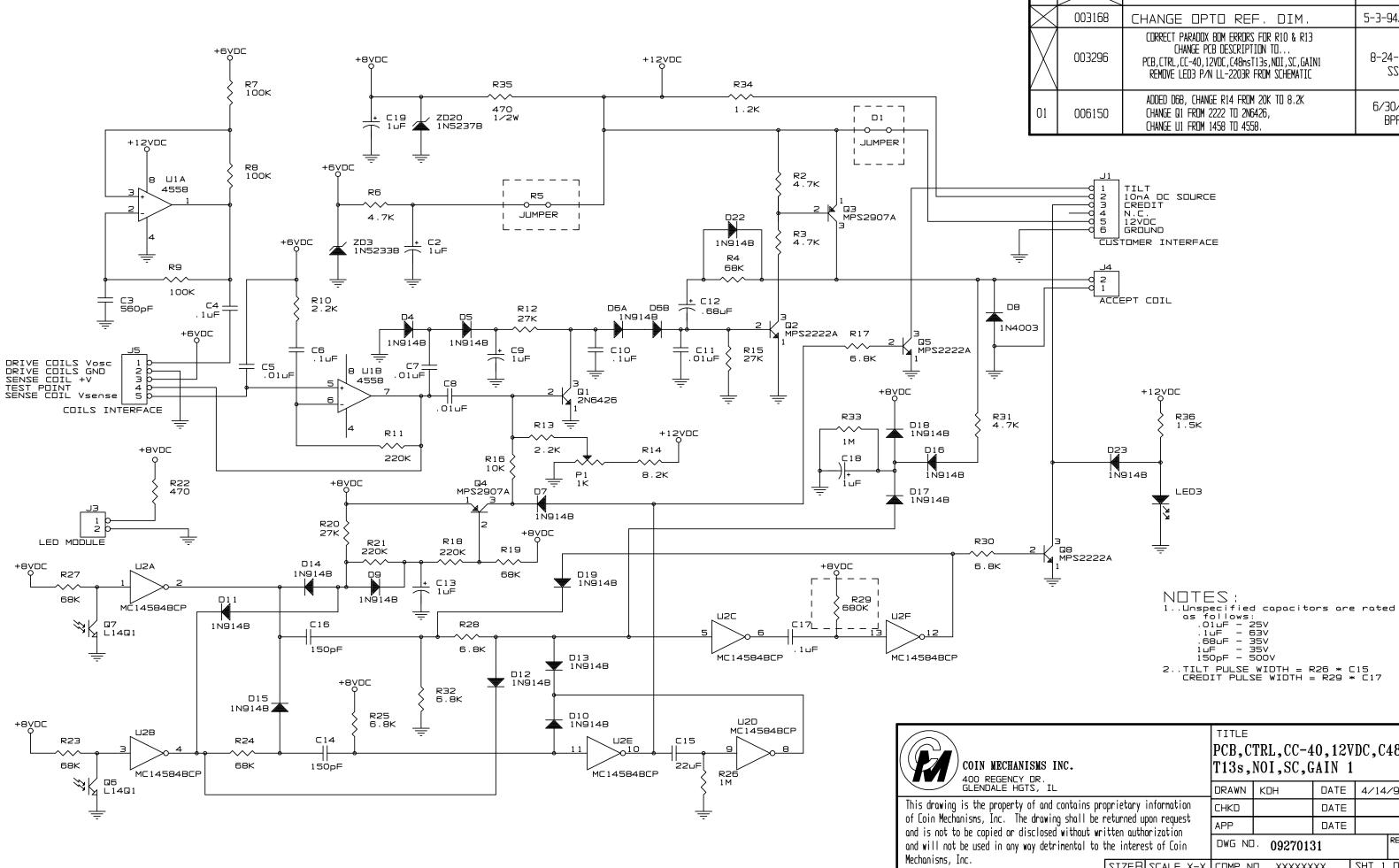
# 6. CHANGE R11 FROM 220K FOR GAIN 1

	TO 100K FOR GAIN 2	
7.	CHANGE R13 FROM 2,2K FOR GAIN	1

			TITL	F				
	TO 100	) FOR	GAIN	2				
/,	LHANGE	: KIJ	FRUM	2 , 2K	FUR	GAIN	1	

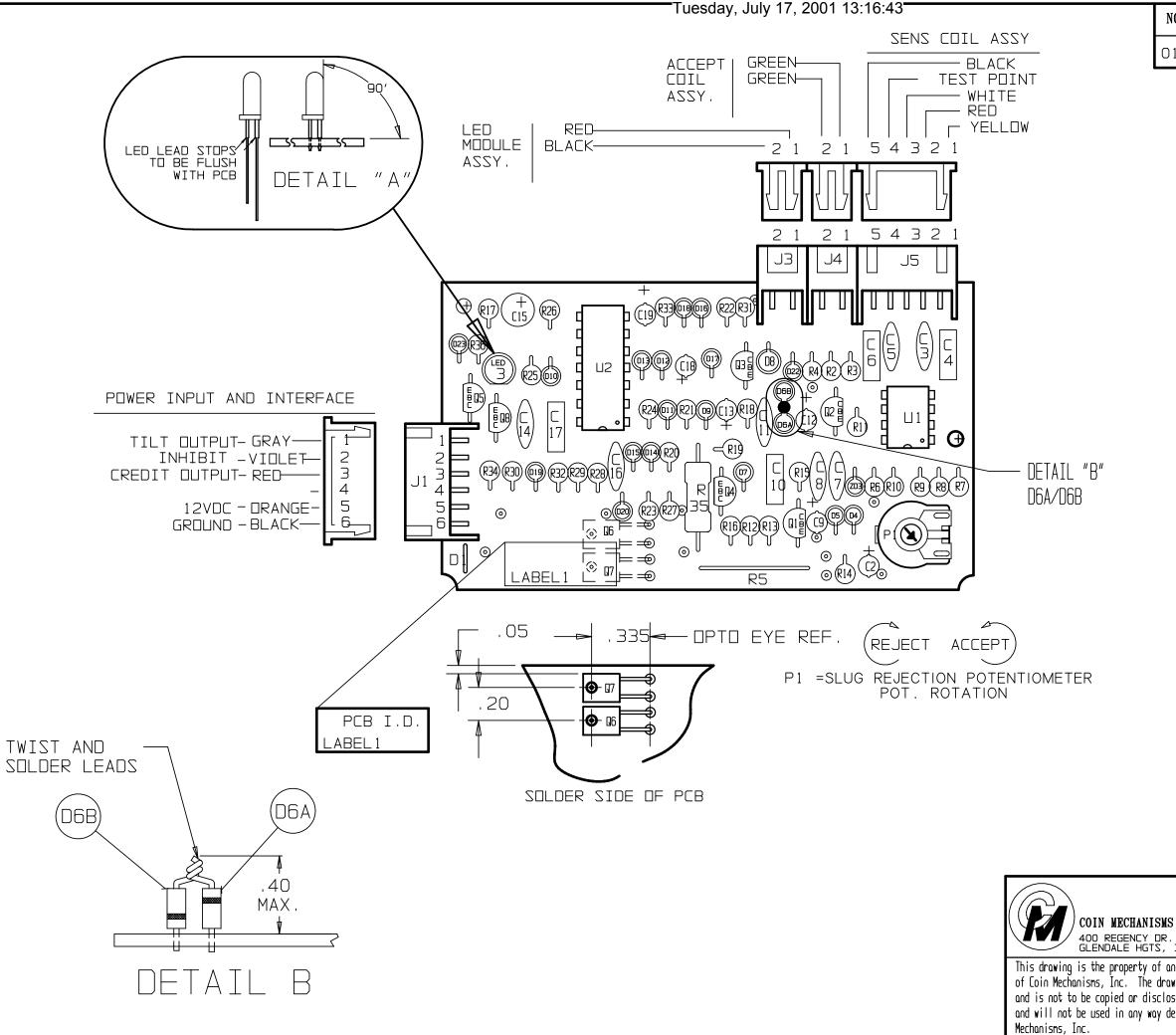
S INC.			TITLE PCB,CTRL,CC-40,AC/DC, C48ms,T13s,NOI,SC,GAIN 1				
IL			DRAWN	22	DATE	5/3/94	
and contains proprietary information awing shall be returned upon request		СНКД		DATE			
awing shall be r osed without wri			APP		DATE		
detrimental to t	the intere	st of Coin	DWG ND. 09270130				
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1	





NO.	ECO NO.	REVISIONS	DATE/BY
00	$\searrow$	NEW RELEASE	5-14-92/KDH
$\langle$	003168	CHANGE OPTO REF. DIM.	5-3-94/SS
$\left\langle \right\rangle$	003296	CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TD PCB,CTRL,CC-40,12VDC,C48nsT13s,NDI,SC,GAIN1 REMOVE LED3 P/N LL-2203R FRDM SCHEMATIC	8-24-94 SS
)1	006150	ADDED D6B, CHANGE R14 FRDM 20K TO 8.2K CHANGE Q1 FRDM 2222 TO 2N6426, CHANGE U1 FRDM 1458 TO 4558.	6/30/00 BPP

IS INC.		TITLE PCB,CTRL,CC-40,12VDC,C48ms, T13s,NOI,SC,GAIN 1				
IL	DRAWN	КОН	DATE	4/14/92		
and contains proprietary information	СНКО		DATE			
awing shall be returned upon request osed without written authorization	APP		DATE			
detrimental to the interest of Coin	DWG NE	0057010		REV 1		
SIZEB SCALE X=>	( COMP N	D. XXXXXX	XX	SHT 1 DF 1		



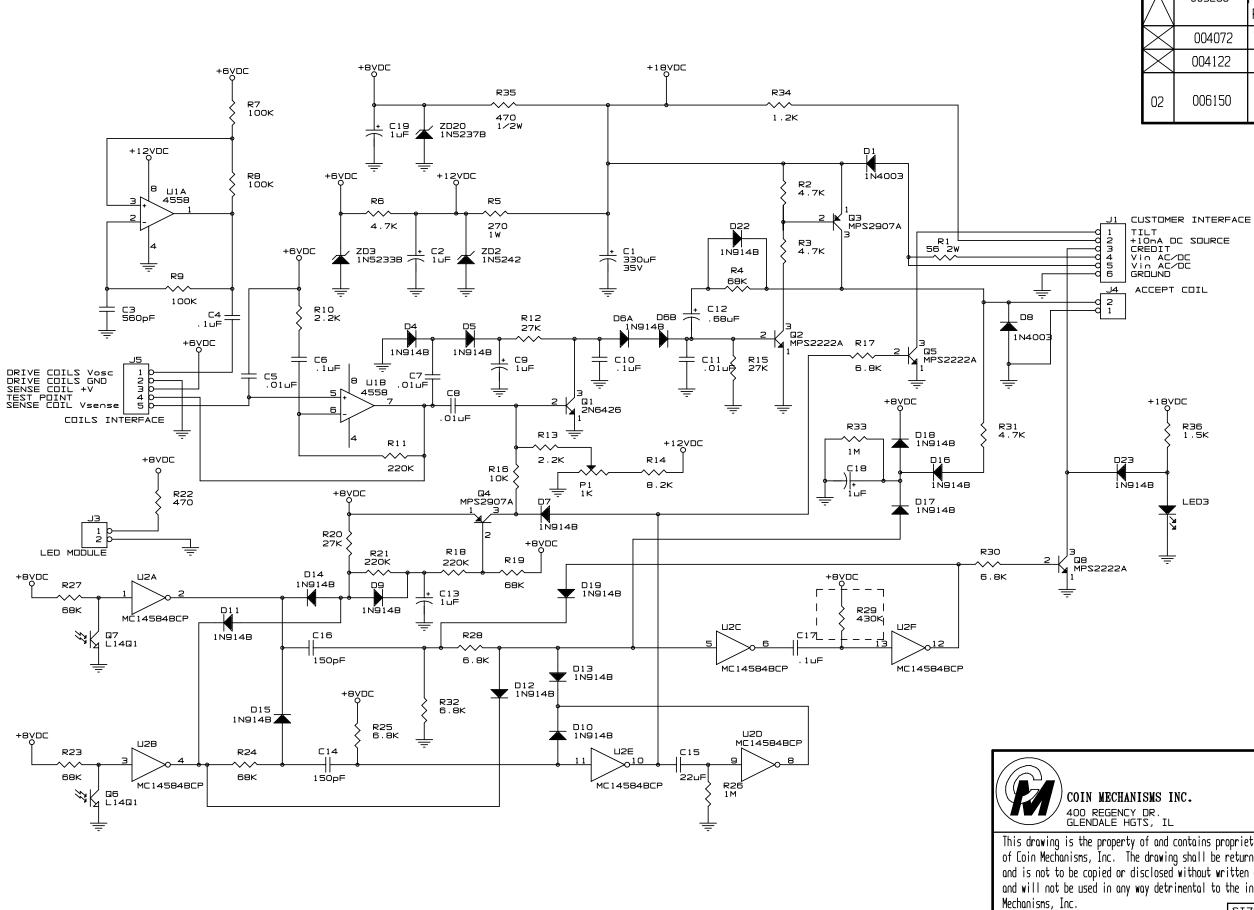
NO.	ECO NO.	REVISIONS	DATE⁄BY
D1	6150	SEE SHEET 1	6/30/00 BPP

NDTES:

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM.ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35IN.. UNLESS SPECIFIED DTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN. .
- 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

S INC.		TITLE PCB,CTRL,CC-40,12VDC,C48ms, T13s,NOI,SC,GAIN 1					
IL		DRAWN	22	DATE	5/3/94		
and contains proprietary information awing shall be returned upon request osed without written authorization		СНКД		DATE			
		APP		DATE			
	the interest of Coin	DWG NE	<sup>rev</sup> 1				
	SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1		

Tuesday, July 17, 2001 13:20:40



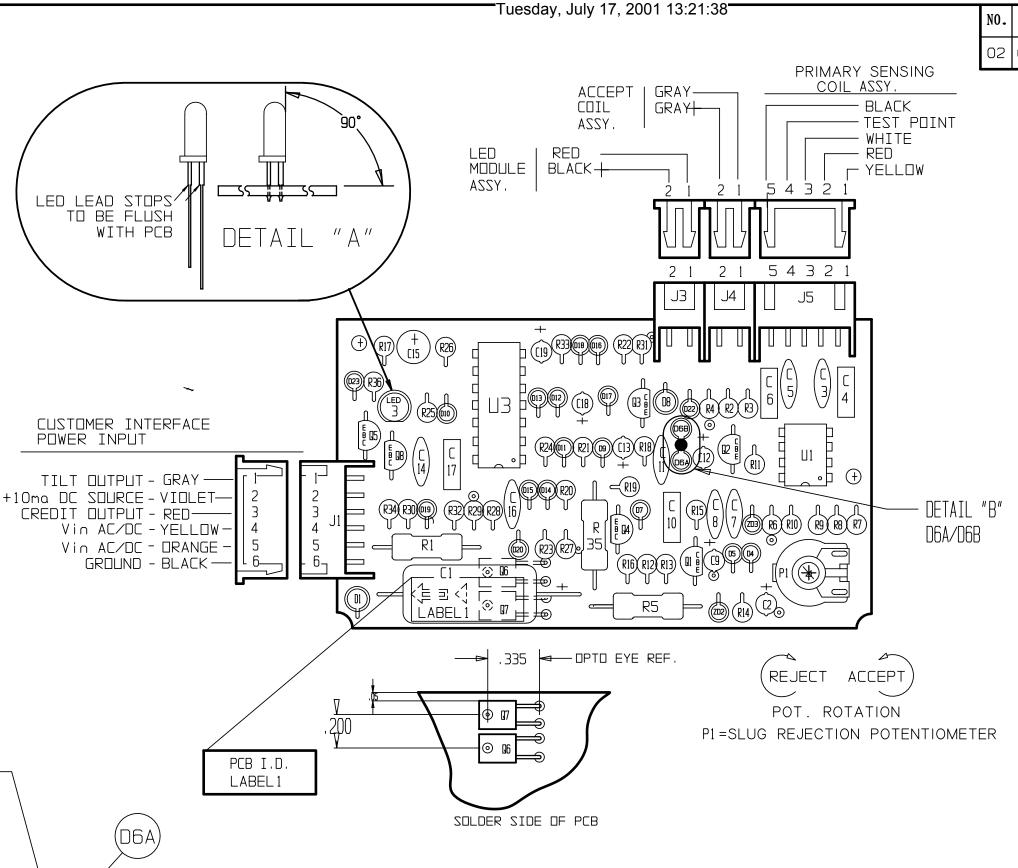
NO.	ECO NO.	REVISIONS	DATE⁄BY
)0	>	NEW RELEASED	5-18-92/KDH
)1	003168	SEE ECD 003168	5-3-94/SS
$\left\langle \right\rangle$	003296	CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TO PCB,CTRL,CC-40,AC/DC,C30msT13s,NDI,SC,GAIN1 REMOVE LED3 P/N LL-2203R FROM SCHEMATIC	8-24-94/SS
$\langle$	004072	TEXT ERROR, CHANGE U3 TO U2 ON SHEET 2	10-25-95⁄SS
<	004122	TEXT ERROR, CORRECTED COLOR ORDER OF J3 ON SHEET 2	2-7-96/LT
)2	006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	6/28/00 BPP

NDTES:

1..Unspecified capacitors are rated as follows: .01uF - 25V .1uF - 63V .68uF - 35V 1uF - 35V 150pF - 500V 2..TILT PULSE WIDTH = R26 \* C15 CREDIT PULSE WIDTH = R29 \* C17

3. REMOVING D1,C1,ZD2,R5 AND INSTALLING JUMPERS FOR D1 AND R5 MAKES THIS A 12VDC OPERATIONAL PCB.

S INC.	TITLE PCB,CTRL,CC-40,AC/DC,C30ms, T13s,NOI,SC,GAIN 1					
ĨL.	DRAWN	КОН	DATE	4/14/92		
and contains proprietary information	СНКО		DATE			
awing shall be returned upon request osed without written authorization	APP		DATE			
detrimental to the interest of Coin	DWG ND. 09270133					
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 OF 1		



TWIST AND

SOLDER LEADS

D6B

.40

MAX.

В

DETAIL



10.	ECO NO.	REVISIONS	DATE/BY
)2	006150	SEE SHEET 1	6/28/00 BPP

NDTES:

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM. ALSO SEE PARTS LIST FOR COMPONENT VALUES.

- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN.. UNLESS SPECIFIED OTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION

4. SOLDERING SHALL BE IN ACCORDANCE

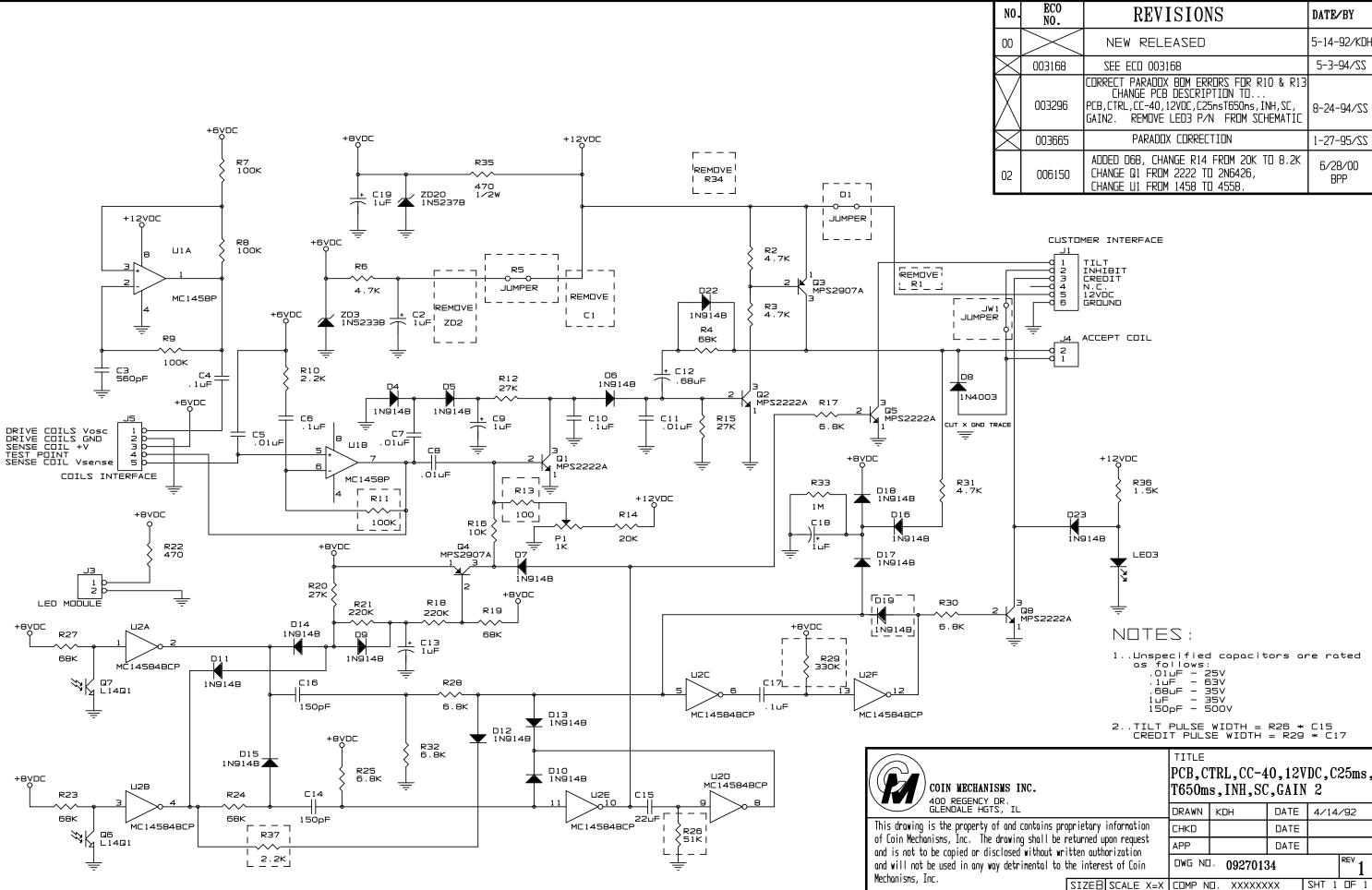
- TO BE .085 IN..

WITH IPC-S-815.

	1108,	S,NUI,SC,GAIN I					
ĨL			DRAWN	22	DATE	5/3/	94
and contains pro			СНКД		DATE		
awing shall be returned upon request osed without written authorization			APP		DATE		
detrimental to t			DWG NO	<sup>1.</sup> 092701:	33		<sup>rev</sup> 2
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1	OF 1

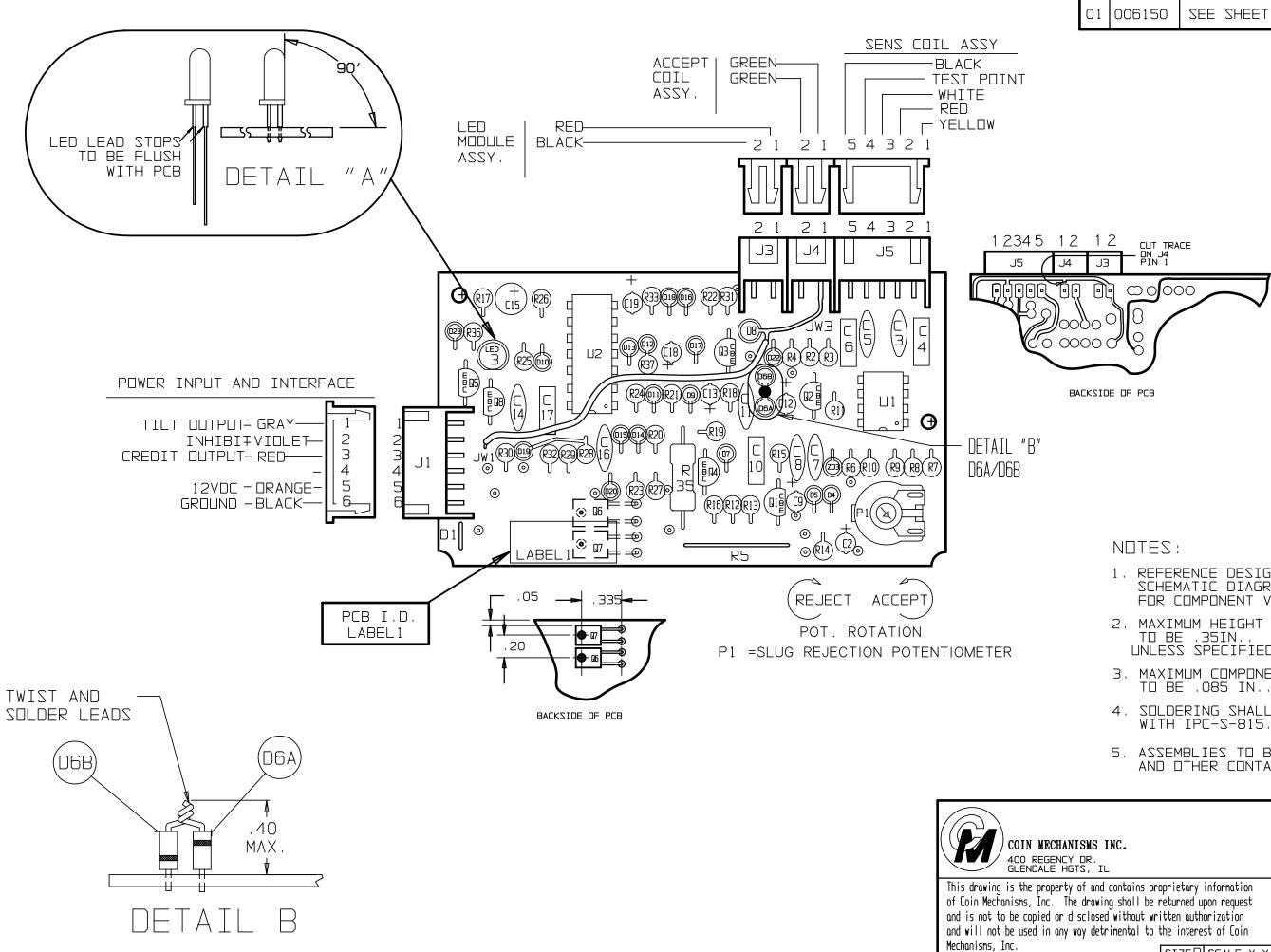
TITLE

Tuesday, July 17, 2001 13:18:05



REVISIONS	DATE⁄BY
NEW RELEASED	5-14-92/KDH
SEE ECO 003168	5-3-94/SS
CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TO PCB,CTRL,CC-40,12VDC,C25nsT650ns,INH,SC, GAIN2. REMOVE LED3 P/N FROM SCHEMATIC	8-24-94/SS
PARADDX CORRECTION	1-27-95/SS
ADDED D6B, CHANGE R14 FRDM 20K TD 8.2K CHANGE D1 FRDM 2222 TD 2N6426, CHANGE U1 FRDM 1458 TD 4558.	6/28/00 BPP



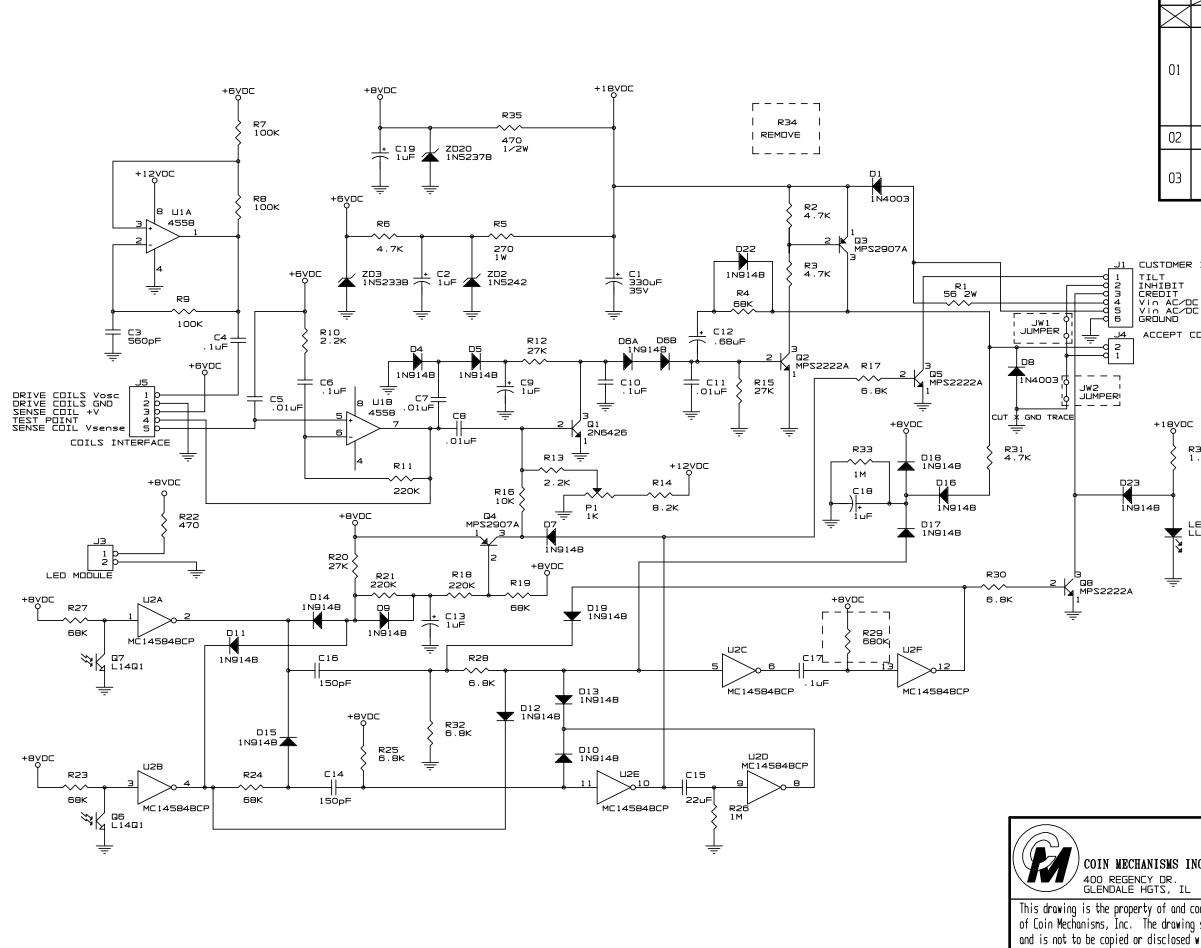


10.	ECO NO.	REVISIONS	DATE/BY
D1	006150	SEE SHEET 1	6/28/00 BPP

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM.ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35IN.. UNLESS SPECIFIED OTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

IS INC.			TITLE PCB,CTRL,CC-40,12VDC,C T650ms,INH,SC,GAIN 2					
? IL			DRAWN	22	DATE	5/3/94		
and contains proprietary information wing shall be returned upon request			СНКД		DATE			
osed without wr	returned u itten outh	pon request	APP		DATE			
detrimental to the interest of Coin			DWG ND. 09270134					
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1		





NO.	ECO NO.	REVISIONS	DATE⁄BY
00	$\searrow$	NEW RELEASED	5-18-92/KDH
$\boxtimes$	486	CHANGED LST OPTO REF. DIM355 WAS31	10/09/92 SS
01	3168	CHANGE R1 FROM 100 1/2W TO 56 2W. CHANGE C1 FROM 220uF 25V TO 330uF 35V. UPDATED LED3 P/N TO LL-2203R. UPDATE TEXT AT J1 PINS 4&5. REVERSE TRACES ON J5 PINS 4&5 SHEET 1. ADD I.D. LABEL (LABEL 1) TO SHEET2 GENERATED SHEET 3 ENG BOM.	5/3/94 SS
02	4921	R29 WAS 330K	9/19/97 BP
03	006150	ADDED D6B, CHANGE R14 FRDM 20K TD 8.2K CHANGE Q1 FRDM 2222 TD 2N6426, CHANGE U1 FRDM 1458 TD 4558.	6/28/00 BPP

CUSTOMER INTERFACE

ACCEPT COIL

R36 1 . 5K

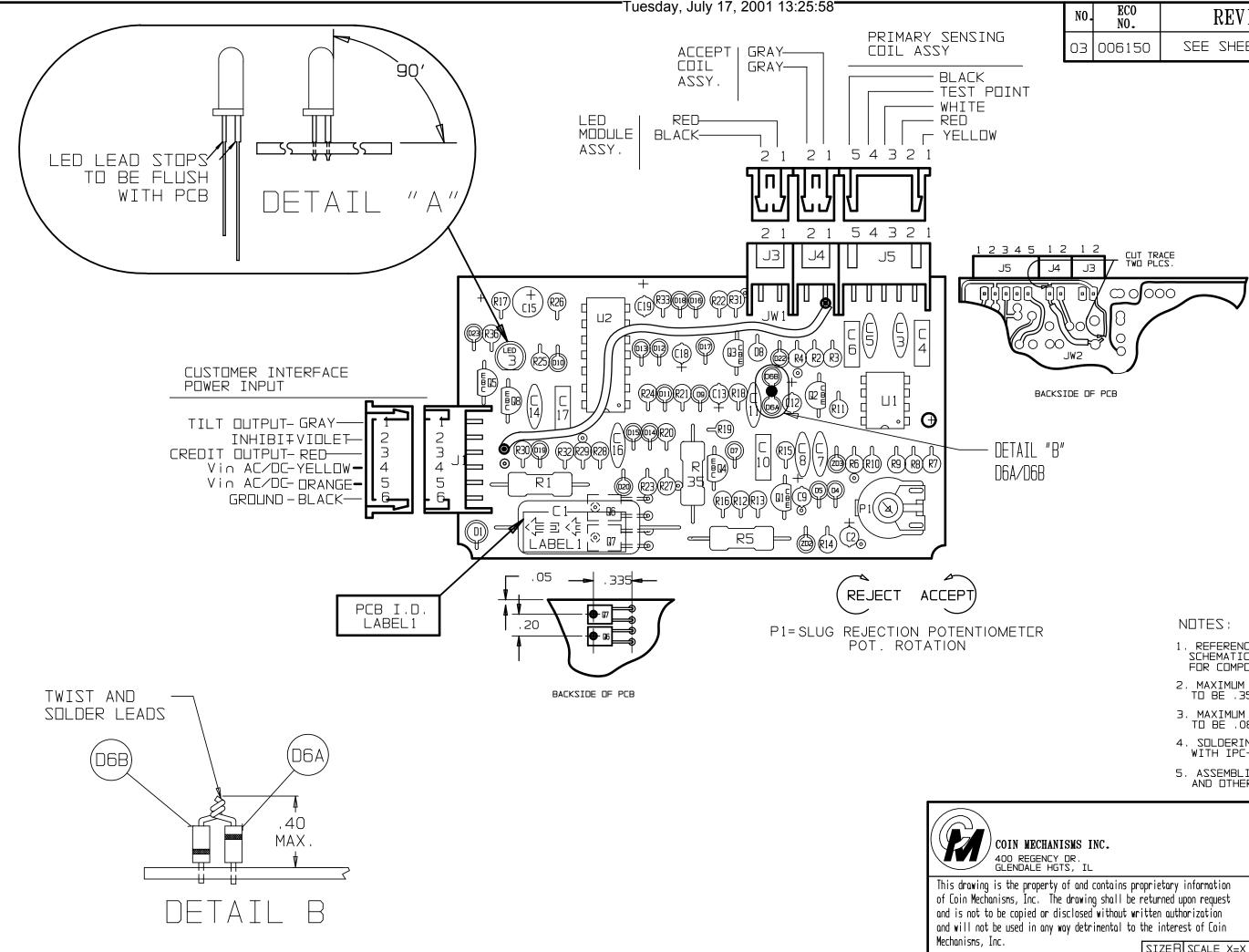
LED3 LL-2203R

NDTES :

1. Unspecified capacitors are rated as follows: .01uF - 25V .1uF - 63V .68uF - 35V 1uF - 35V 150pF - 500V

- 2..TILT PULSE WIDTH = R26 \* C15 CREDIT PULSE WIDTH = R29 \* C17 3. REMOVING D1,C1,ZD2,R5 AND INSTALLING JUMPERS FOR D1 AND R5 MAKES THIS A 12VDC OPERATIONAL PCB.

COIN MECHANISMS INC.			TITLE PCB,CTRL,CC-40,AC/DC,C48ms, T13s,INH HI,SC,GAIN 1				
400 REGENCY DR. GLENDALE HGTS, IL			DRAWN	КОН	DATE	4/14/92	
This drawing is the property of and contains pr	oprietary	information	СНКД		DATE		
of Coin Mechanisms, Inc. The drawing shall be and is not to be copied or disclosed without wr	itten outh	pon reques⊽ orizotioo	APP		DATE		
and will not be used in any way detrimental to Mechanisms, Inc.			DWG ND. 09270135			$^{\text{Rev}}3$	
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1	

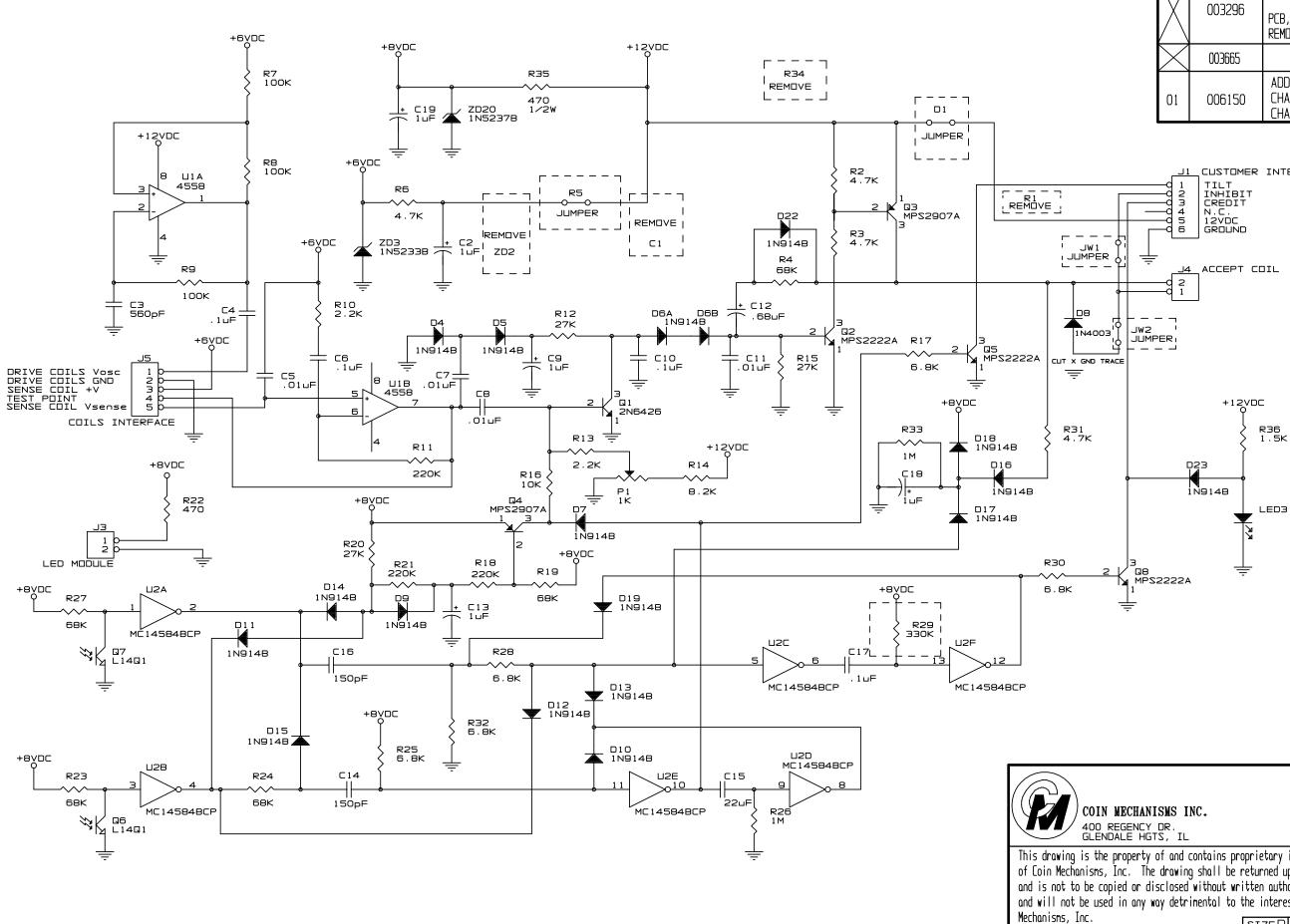


NO.	ECO NO.	REVISIONS	DATE⁄BY
23	006150	SEE SHEET 1	6/30/00 BPP

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM. ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHÉ ON ANY COMPONENT TO BE .35 IN..
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

IS INC.			TITLE PCB,CTRL,CC-40,AC/DC,C48ms, T13s,INH HI,SC,GAIN 1				
IL		DRAWN	KDH	DATE	4/14/92		
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		CHKD		DATE			
		APP		DATE			
		DWG ND. 09270135			$^{\text{Rev}}3$		
SIZEB SCALE	X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1		

Tuesday, July 17, 2001 13:22:46

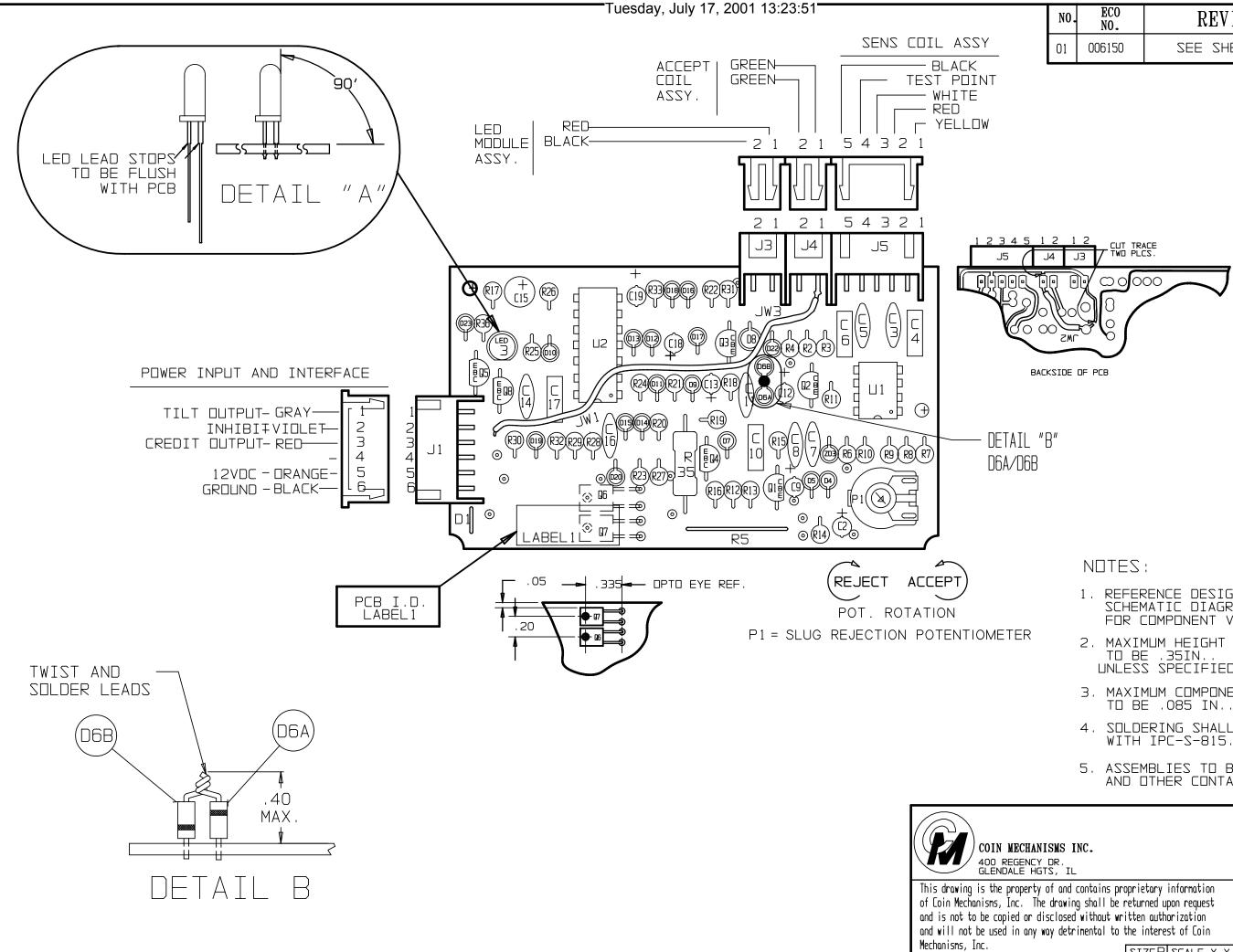


NO.	ECO NO.	REVISIONS	DATE⁄BY
00	$\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{$	NEW RELEASE	5-1-92/KDH
$\times$	003168	SEE ECD 003168	5-3-94/SS
$\left \right\rangle$	003296	CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TD PCB,CTRL,CC-40,12VDC,C25msT13s,INH,SC,GAIN1 REMOVE LED3 P/N LL-2203R FROM SCHEMATIC	8-24-94/SS
$\times$	003665	PARADDX DATABASE UPDATED	2-1-95⁄SS BY LT 4-7-97
01	006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	6/28/00 BPP

J1 CUSTOMER INTERFACE

NDTES: 1...Unspecified capacitors are rated as follows: ...UF - 25V ...UF - 63V ...68uF - 35V 1uF - 35V 150F - 500V 2. TIT PU SE WIDTH - R25 \* C15 2. TILT PULSE WIDTH = R26 \* C15 CREDIT PULSE WIDTH = R29 \* C17 3. REMOVING D1,C1,ZD2,R5 AND INSTALLING JUMPERS FOR D1 AND R5 MAKES THIS A 12VDC OPERATIONAL PCB.

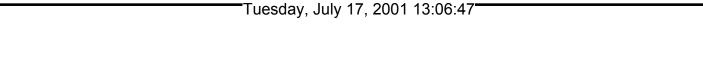
S INC.		TITLE PCB,CTRL,CC-40,12VDC,C25ms, T13s,INH,SC,GAIN 1				
		DRAWN	22	DATE	5/1/92	
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		CHKD		DATE		
		APP		DATE		
		DWG ND. 09270137			<sup>rev</sup> 1	
	SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1	

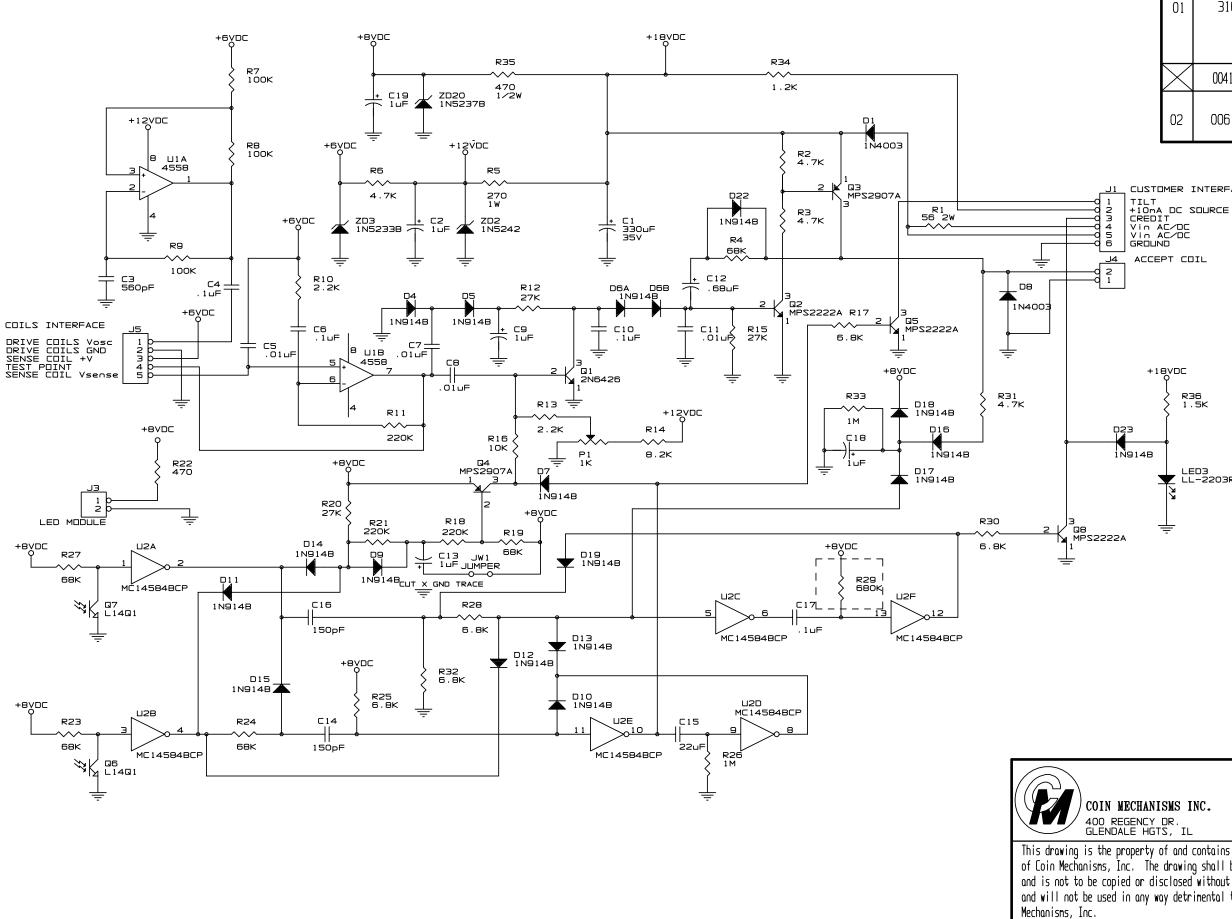


NO.	ECO NO.	REVISIONS	DATE⁄BY
01	006150	SEE SHEET 1	6/30/00 BPP

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM, ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35IN.. UNLESS SPECIFIED OTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

	TITLE PCB,CTRL,CC-40,12VDC,C25ms, T13s,INH,SC,GAIN 1				
IL	DRAWN	22	DATE	5/1/92	
and contains proprietary information	СНКД		DATE		
awing shall be returned upon request osed without written authorization	APP		DATE		
detrimental to the interest of Coin	DWG ND. 09270137			<sup>rev</sup> 1	
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1	





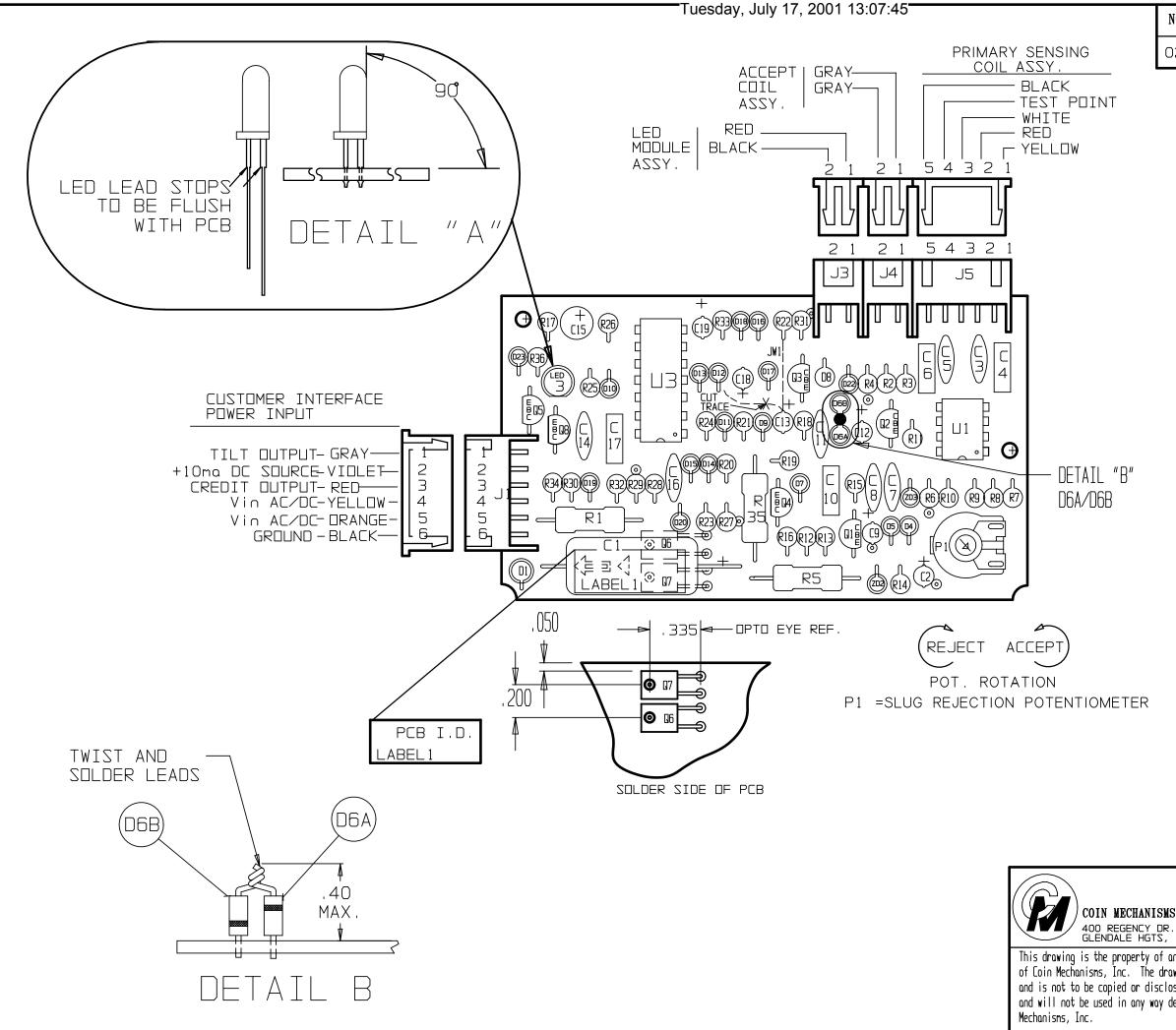
NO.	ECO NO.	REVISIONS	DATE⁄BY
00	$\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{\mathbf{$	NEW RELEASE	10-16-92 KDH
01	3168	CHANGE R1 FROM 100 1/2W TO 56 2W CHANGE C1 FROM 220uF 25V TO 330uF 35V UPDATED LED3 P/N TO LL-2203R UPDATE TEXT AT J1 PINS 4 AND 5 REVERSE TRACES on J5 PINS 4 and 5 sheet 1 ADD JUMPER JW1 TO SHEET 1 ADD I.D. LABEL (LABEL1) TO SHEET 2 GENERATED SHEET 3 ENG BDM	5/3/94 SS
imes	004122	TEXT ERROR, CORRECTED COLOR ORDER OF J3 SHEET 2	2-8-96/LT
02	006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	6/28/00 BPP

CUSTOMER INTERFACE

R36 1 . 5K

NDTES: 1...Unspecified capacitors are rated as follows: ...UF - 25V ...UF - 63V ...68uF - 35V 1uF - 35V 150F - 500V 2. TIT PU SE WIDTH - R25 \* C15 2...TILT PULSE WIDTH = R26 \* C15 CREDIT PULSE WIDTH = R29 \* C17 3. REMOVING D1,C1,ZD2,R5 AND INSTALLING JUMPERS FOR D1 AND R5 MAKES THIS A 12VDC OPERATIONAL PCB.

S INC.	TITLE PCB,CTRL,CC-40,AC/DC,C48ms, T13s,NOI,SC,GAIN 1,UDA				
IL	DRAWN	КОН	DATE	4/14/92	
and contains proprietary information	СНКО		DATE		
awing shall be returned upon request osed without written authorization	APP		DATE		
detrimental to the interest of Coin	DWG NE	<sup>1</sup> . 0927014	48	REV 2	
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1	



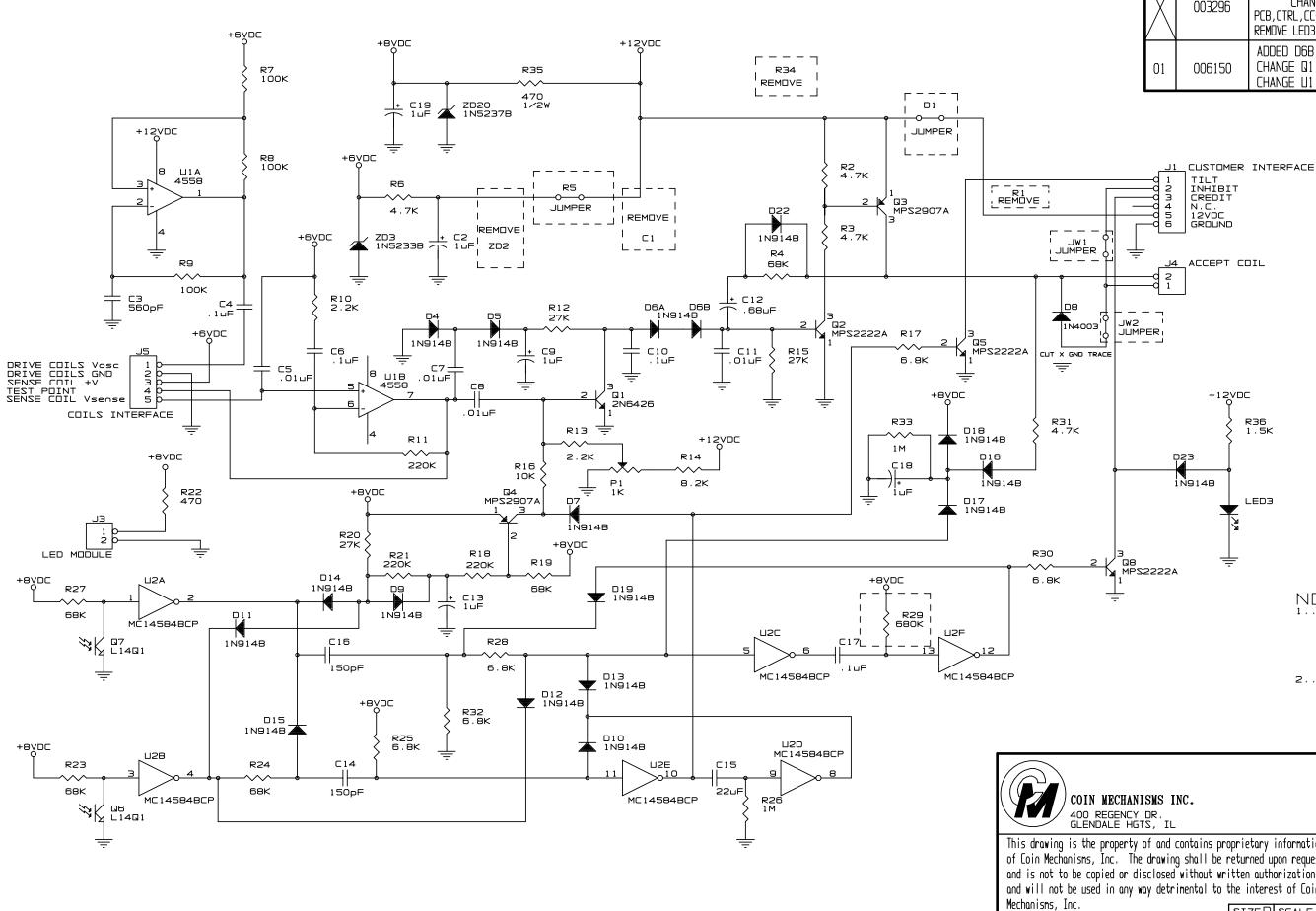
NO.	ECO NO.	REVISIONS	DATE⁄BY
02	006150	SEE SHEET 1	6/30/00 BPP

NDTES:

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM.ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN..
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

S INC.			TITLE PCB,CTRL,CC-40,AC/DC,C48ms, T13s,NOI,SC,GAIN 1,UDA			
IL		DRAWN	КОН	DATE	4/14/92	
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		СНКД		DATE		
		APP		DATE		
			DWG ND. 09270148			REV 2
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1

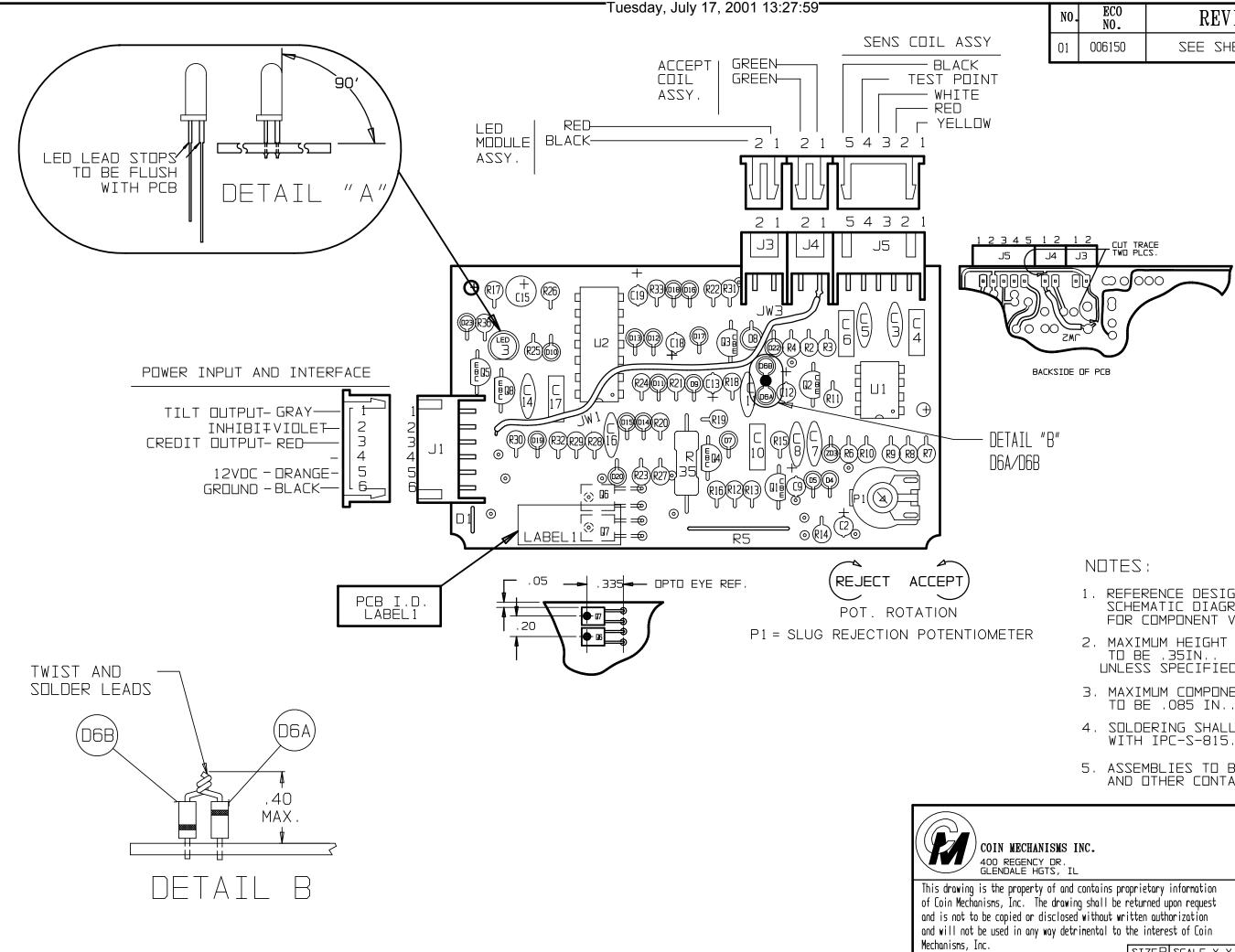
Tuesday, July 17, 2001 13:27:02



NO.	ECO NO.	REVISIONS	DATE/BY
00	003168	NEW RELEASE PCB DINERATED FROM VERSION 09270137 WITH R29 @680K	5/3/94 SS
$\left\langle \right\rangle$	003296	CORRECT PARADOX BOM ERRORS FOR R10 & R13 CHANGE PCB DESCRIPTION TO PCB,CTRL,CC-40,12VDC,C48msT13s,INH,SC,GAIN1 REMOVE LED3 P/N FROM SCHEMATIC	8-24-94/SS
)1	006150	ADDED D6B, CHANGE R14 FRDM 20K TD 8.2K CHANGE D1 FRDM 2222 TD 2N6426, CHANGE D1 FRDM 1458 TD 4558.	6/28/00 BPP

NDTES: 1...Unspecified capacitors are rated as follows: ...UF - 25V ...UF - 63V ...68uF - 35V 1uF - 35V 150F - 500V 2. TIT PU SE WIDTH - R25 \* C15 2...TILT PULSE WIDTH = R26 \* C15 CREDIT PULSE WIDTH = R29 \* C17

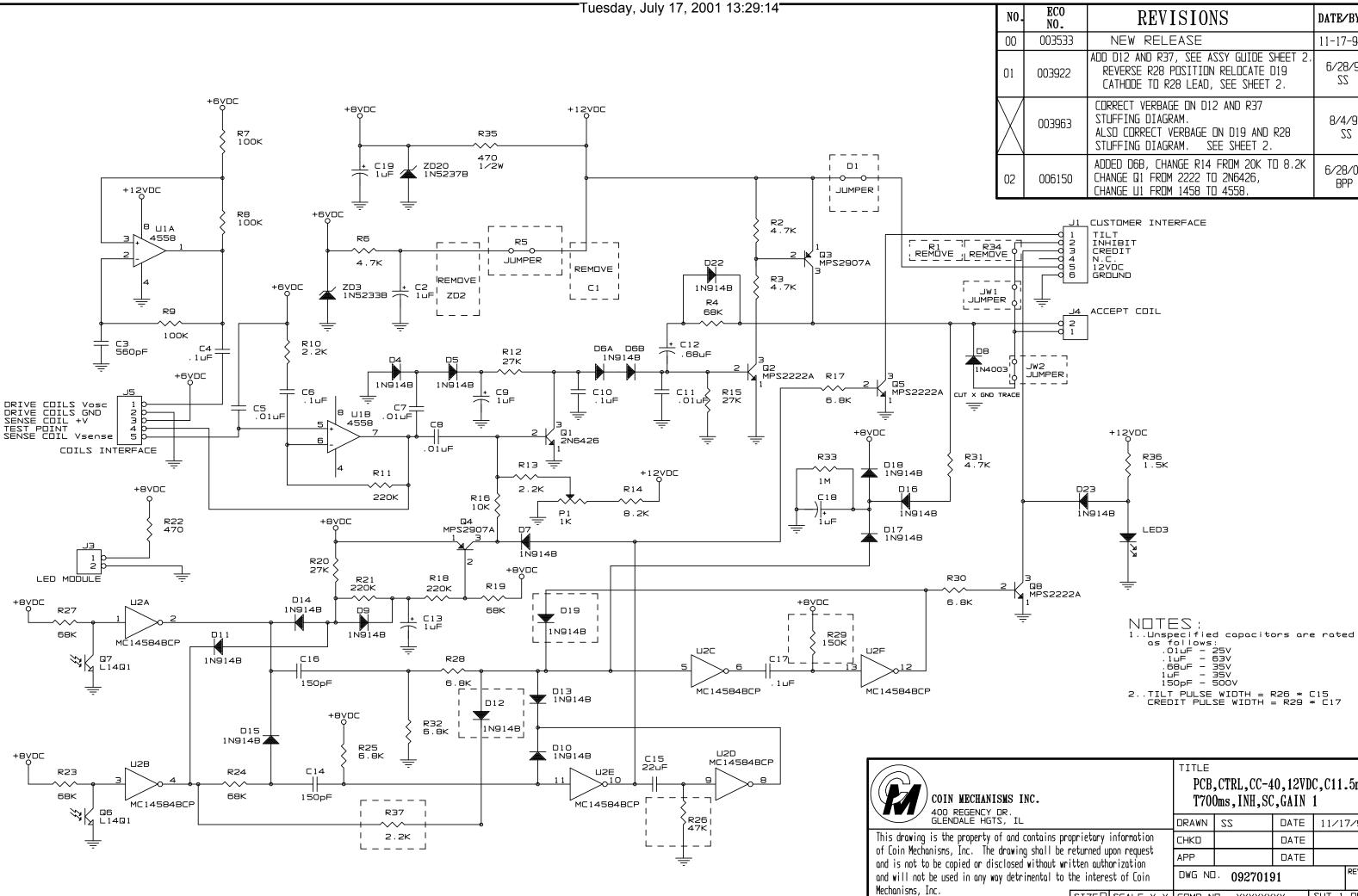
S INC.	TITLE PCB,CTRL,CC-40,12VDC,C48ms, T13s,INH,SC,GAIN 1				
IL	DRAWN	22	DATE	5/1/92	
and contains proprietary information	СНКД		DATE		
awing shall be returned upon request osed without written authorization	APP		DATE		
detrimental to the interest of Coin	DWG NE	<b>092701</b> 4	19	<sup>rev</sup> 1	
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 OF 1	



NO.	ECO NO.	REVISIONS	DATE⁄BY
01	006150	SEE SHEET 1	6/30/00 BPP

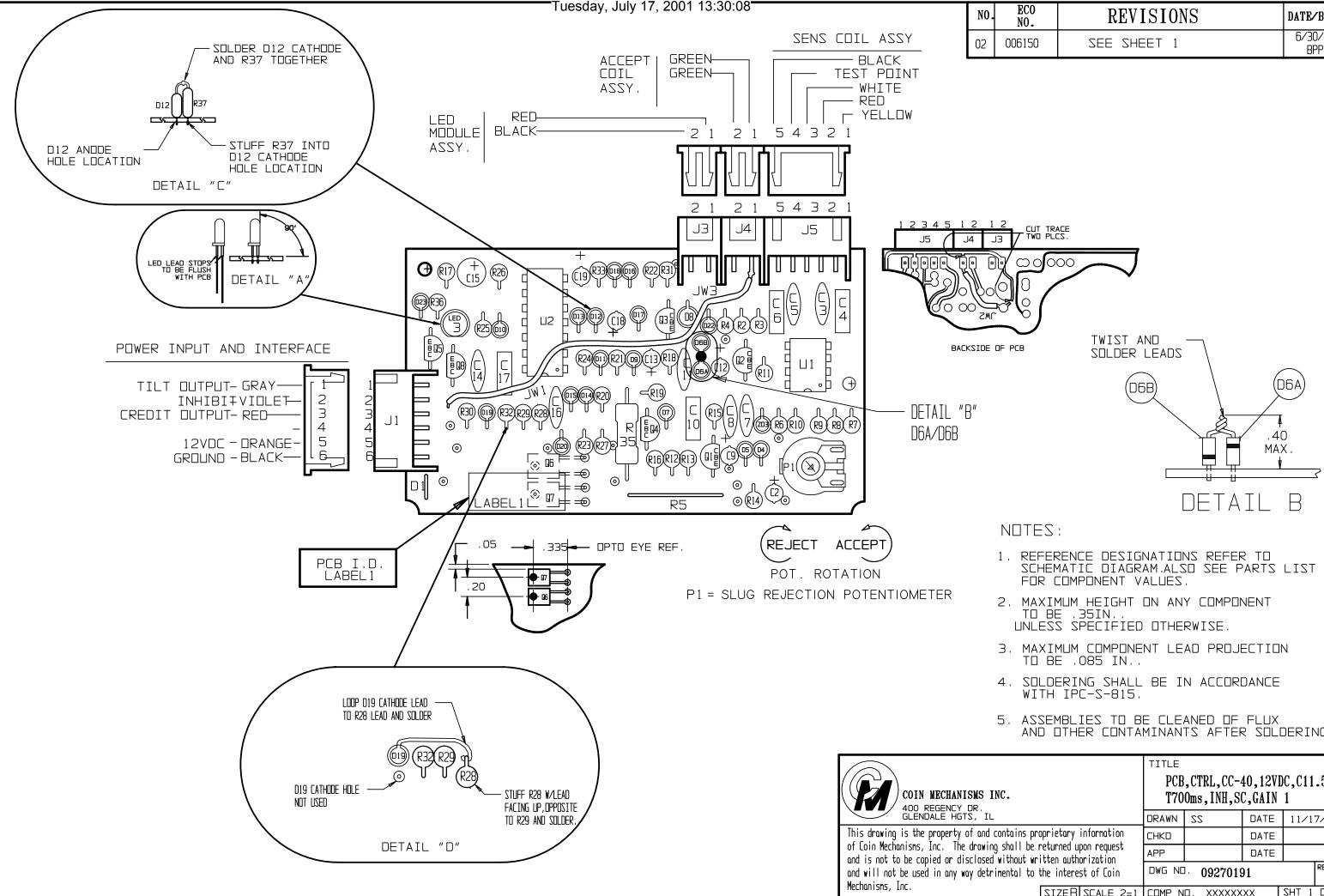
- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM, ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35IN.. UNLESS SPECIFIED OTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

S INC.		TITLE PCB,CTRL,CC-40,12VDC,C48ms, T13s,INH,SC,GAIN 1				
		RAWN	22	DATE	5/1/92	
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		IKD		DATE		
		۳P		DATE		
		DWG ND. 09270149			<sup>rev</sup> 1	
SIZEB SCALE X	=X CC	IMP N	I. XXXXXX	XX	SHT 1 OF 1	



NO.	ECO NO.	REVISIONS	DATE⁄BY
00	003533	NEW RELEASE	11-17-94/SS
)1	003922	ADD D12 AND R37, SEE ASSY GUIDE SHEET 2. REVERSE R28 POSITION RELOCATE D19 CATHODE TO R28 LEAD, SEE SHEET 2.	6/28/95 SS
$\left\langle \right\rangle$	003963	CORRECT VERBAGE ON D12 AND R37 STUFFING DIAGRAM. ALSO CORRECT VERBAGE ON D19 AND R28 STUFFING DIAGRAM. SEE SHEET 2.	8/4/95 SS
)2	006150	ADDED D6B, CHANGE R14 FRDM 20K TD 8.2K CHANGE D1 FRDM 2222 TD 2N6426, CHANGE D1 FRDM 1458 TD 4558.	6/28/00 BPP

IS INC.		TITLE PCB,CTRL,CC-40,12VDC,C11.5ms, T700ms,INH,SC,GAIN 1				
			DRAWN	22	DATE	11/17/94
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		СНКД		DATE		
		APP		DATE		
		DWG ND. 09270191			REV 2	
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1

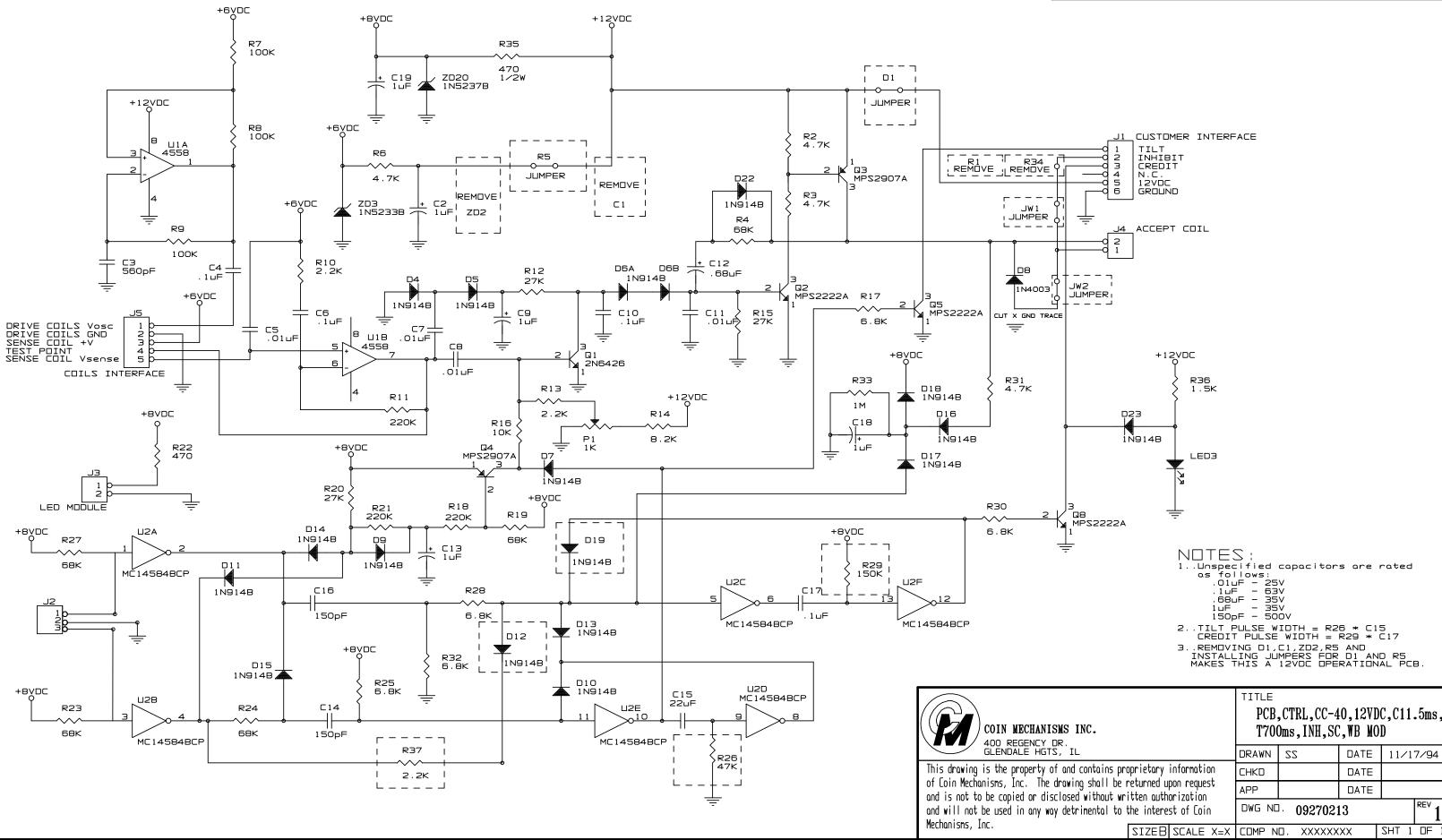


NO.	ECO NO.	REVISIONS	DATE⁄BY
02	006150	SEE SHEET 1	6/30/00 BPP

- AND OTHER CONTAMINANTS AFTER SOLDERING.

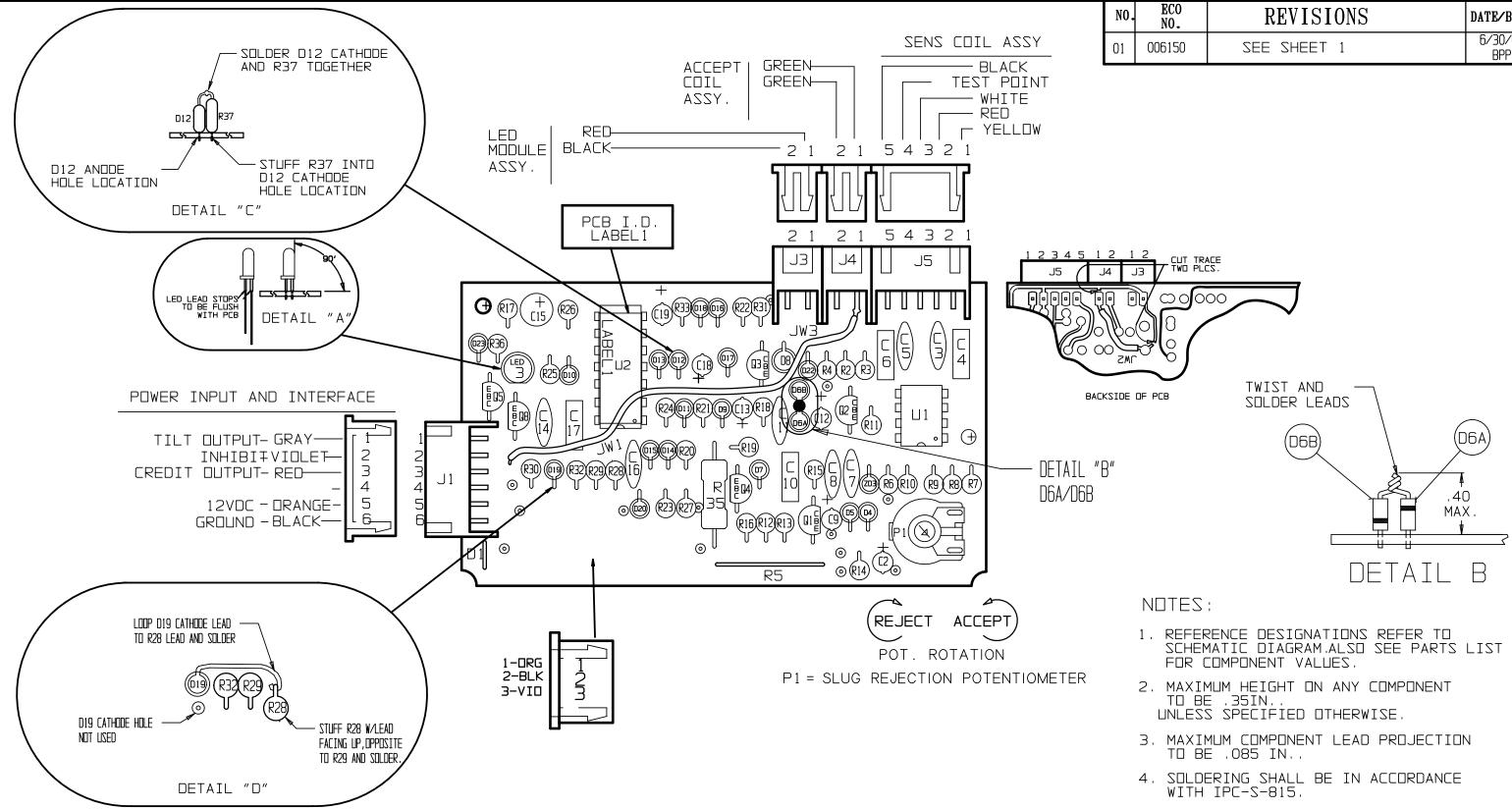
S INC.	TITLE PCB,CTRL,CC-40,12VDC,C11.5ms, T700ms,INH,SC,GAIN 1				
IL	DRAWN	22	DATE	11/17/94	
and contains proprietary information	СНКД		DATE		
awing shall be returned upon request osed without written authorization	APP		DATE		
detrimental to the interest of Coin	DWG ND. 09270191			REV 2	
SIZEB SCALE 2=1	COMP N	D. XXXXXX	XX	SHT 1 DF 1	

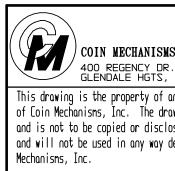
00 \_ Ω



NO.	ECO NO.	REVISIONS	DATE⁄BY
00	004200	NEW RELEASE MOD OF 09270191	4/17/96/LT
)1	006150	ADDED D6B, CHANGE R14 FRDM 20K TD 8.2K CHANGE D1 FRDM 2222 TD 2N6426, CHANGE D1 FRDM 1458 TD 4558.	6/28/00 BPP

S INC.		TITLE PCB,CTRL,CC-40,12VDC,C11.5ms, T700ms,INH,SC,WB MOD					
		DRAWN	22	DATE	11/1	7/94	
and contains proprietary information awing shall be returned upon request osed without written authorization detrimental to the interest of Coin		СНКД		DATE			
		APP		DATE			
		DWG ND. 09270213				<sup>REV</sup> 1	
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1	OF 1

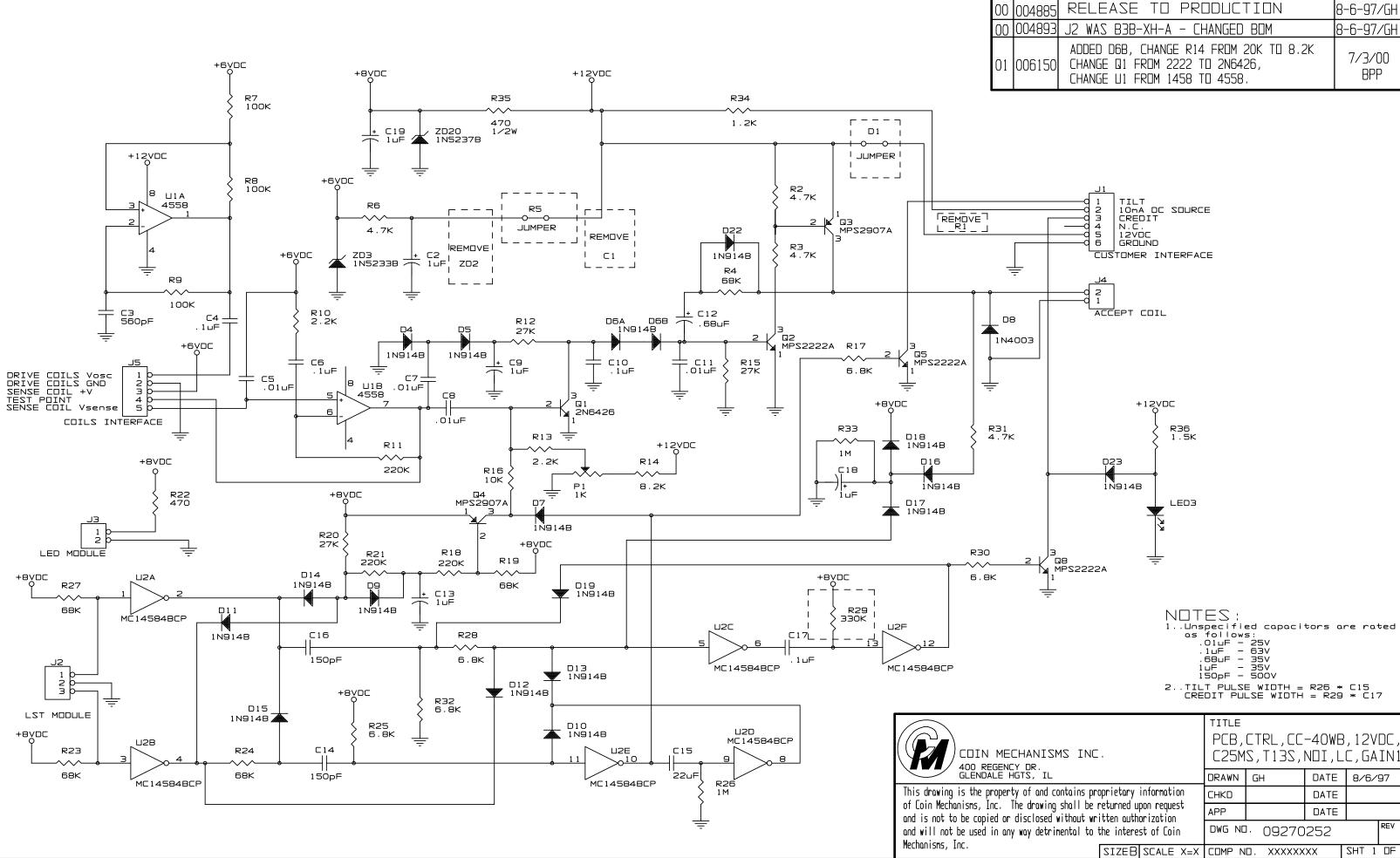




NO.	ECO NO.	REVISIONS	DATE⁄BY
01	006150	SEE SHEET 1	6/30/00 BPP

- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

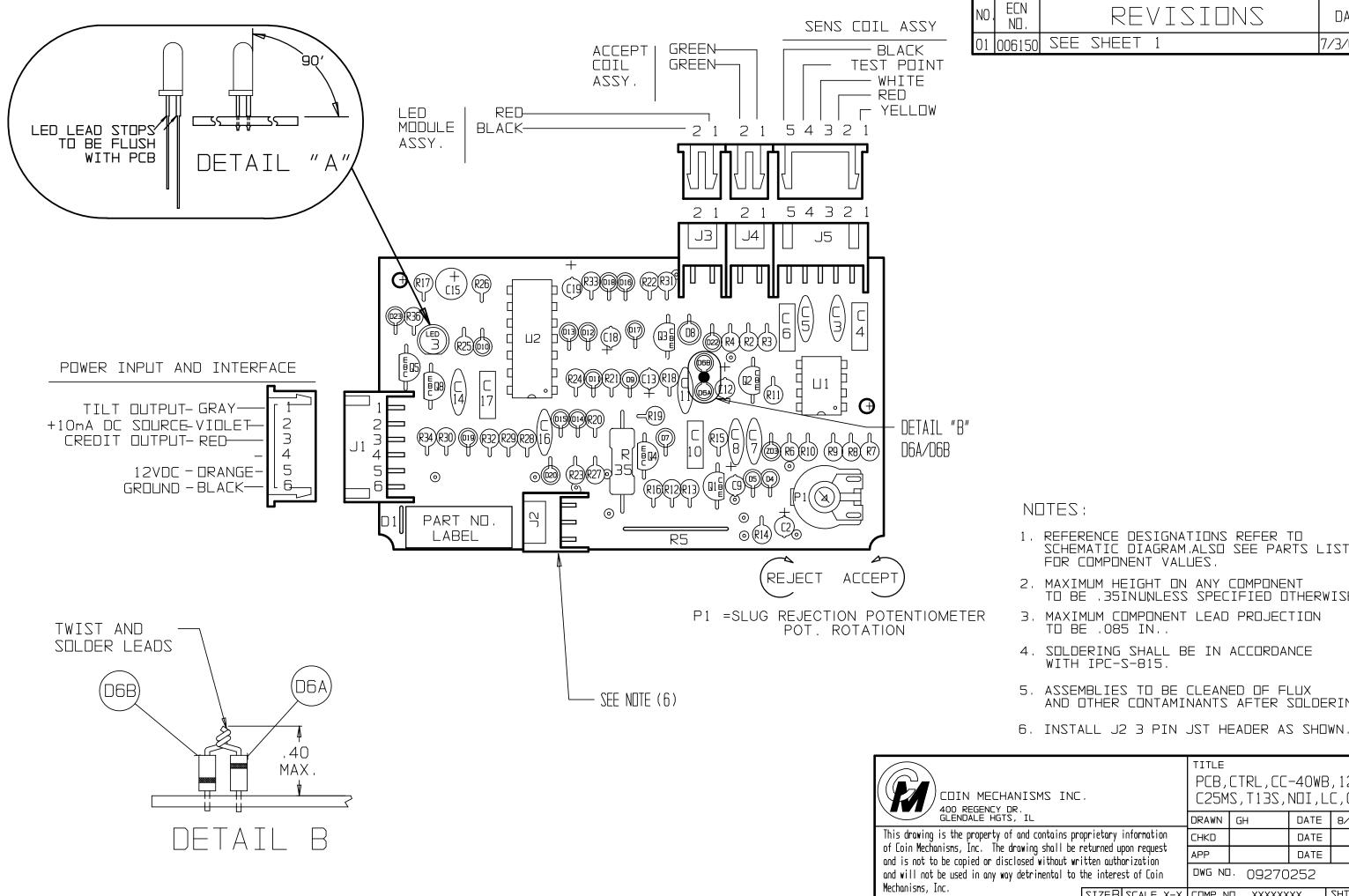
	TITLE PCB,CTRL,CC-40,12VDC,C11.5ms, T700ms,INH,SC,WB MOD			
IS INC.				
IL	DRAWN	LT	DATE	4/17/96
and contains proprietary information	СНКД		DATE	
awing shall be returned upon request osed without written authorization	APP		DATE	
detrimental to the interest of Coin	DWG NE	0 <b>92702</b> 1	13	<sup>rev</sup> 1
SIZEB SCALE 2=1	COMP N	D. XXXXXX	XX	SHT 1 DF 1



ECN ND.	REVISIONS	DATE/BY
004885	RELEASE TO PRODUCTION	8-6-97/GH
004893	J2 WAS B3B-XH-A - CHANGED BOM	8-6-97/GH
006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	7/3/00 BPP

NO

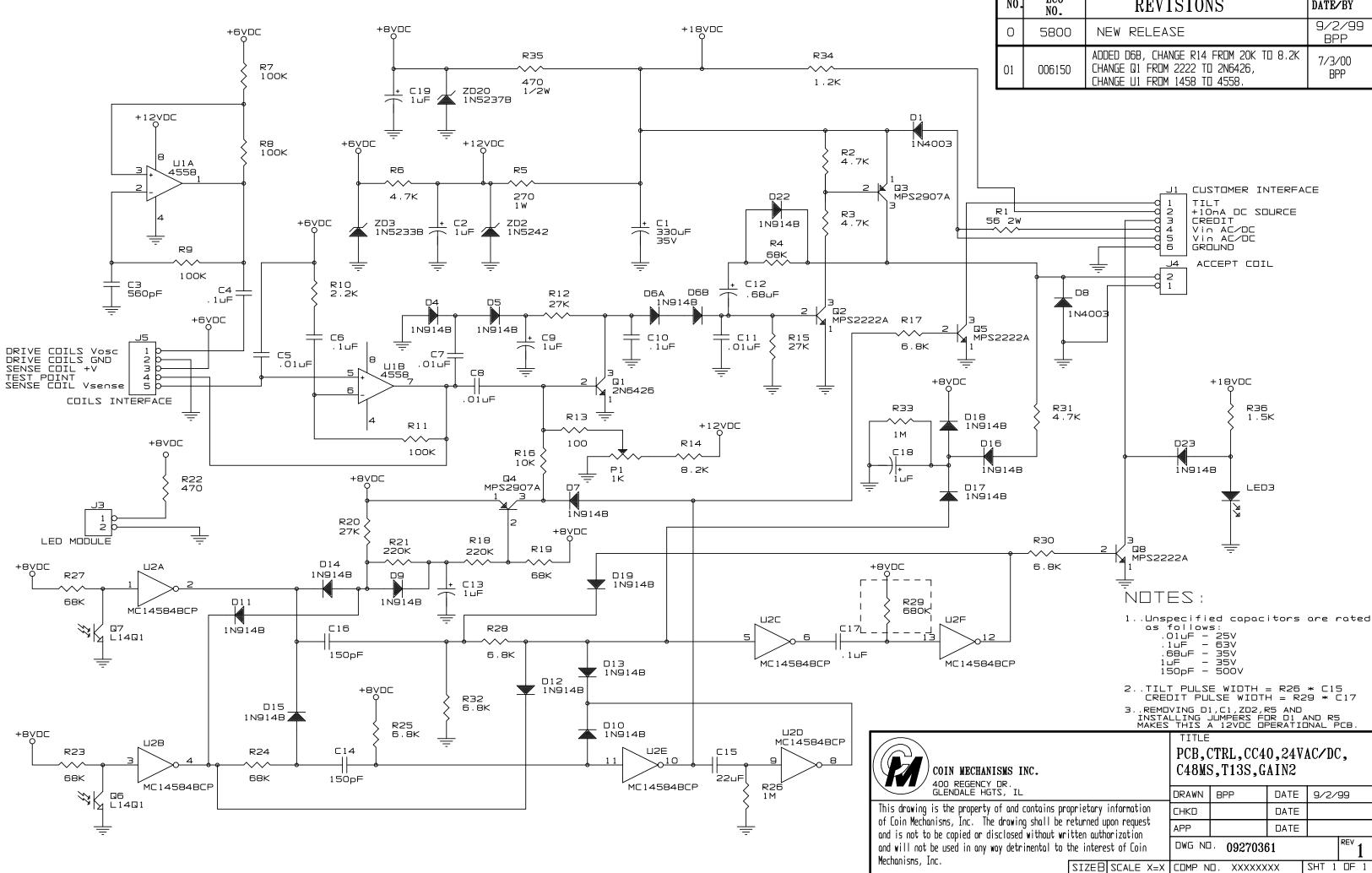
VISMS INC.	TITLE PCB,CTRL,CC-40WB,12VDC, C25MS,T13S,NDI,LC,GAIN1				
IL	DRAWN	GH	DATE	8/6/97	
and contains proprietary information	СНКД		DATE		
awing shall be returned upon request osed without written authorization	APP		DATE		
detrimental to the interest of Coin	DWG ND. 09270252				
SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 3	



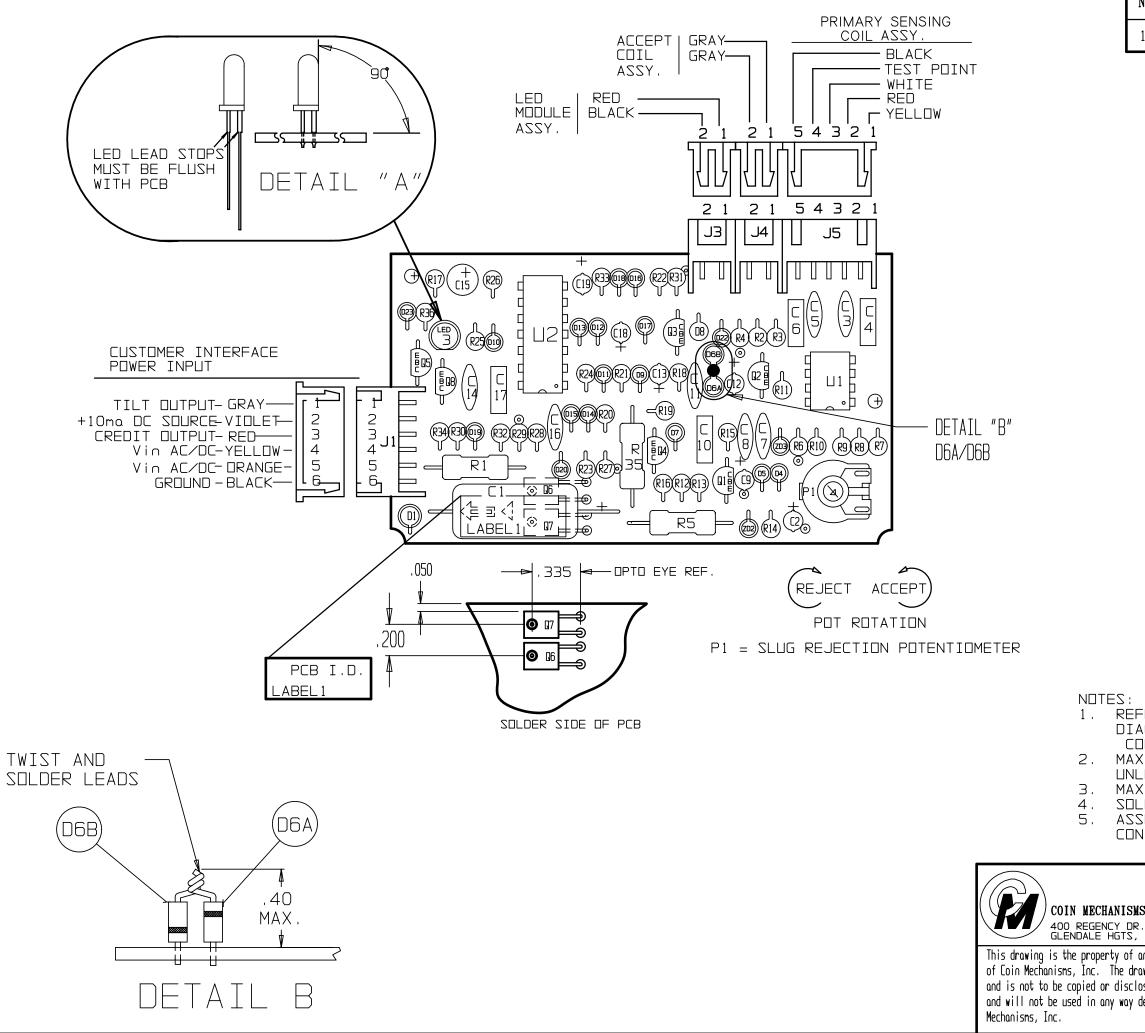
NISMS INC.		TITLE PCB,CTRL,CC-40WB,12VDC, C25MS,T13S,NDI,LC,GAIN1				
IL		DRAWN	GH	DATE	8/6/97	
and contains proprietary information wing shall be returned upon request used without written authorization		СНКД		DATE		
		APP		DATE		
	ne interest of Coin	DWG ND. 09270252				
	SIZEB SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 3	

AND OTHER CONTAMINANTS AFTER SOLDERING.

- WITH IPC-S-815. 5. ASSEMBLIES TO BE CLEANED OF FLUX
- TO BE .085 IN.. 4. SOLDERING SHALL BE IN ACCORDANCE
- 3. MAXIMUM COMPONENT LEAD PROJECTION
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35INUMLESS SPECIFIED OTHERWISE.
- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM.ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- NDTES:



NO.	ECO NO.	REVISIONS	DATE⁄BY
0	5800	NEW RELEASE	9/2/99 BPP
)1	006150	ADDED D6B, CHANGE R14 FRDM 20K TD 8.2K CHANGE D1 FRDM 2222 TD 2N6426, CHANGE U1 FRDM 1458 TD 4558.	7/3/00 BPP

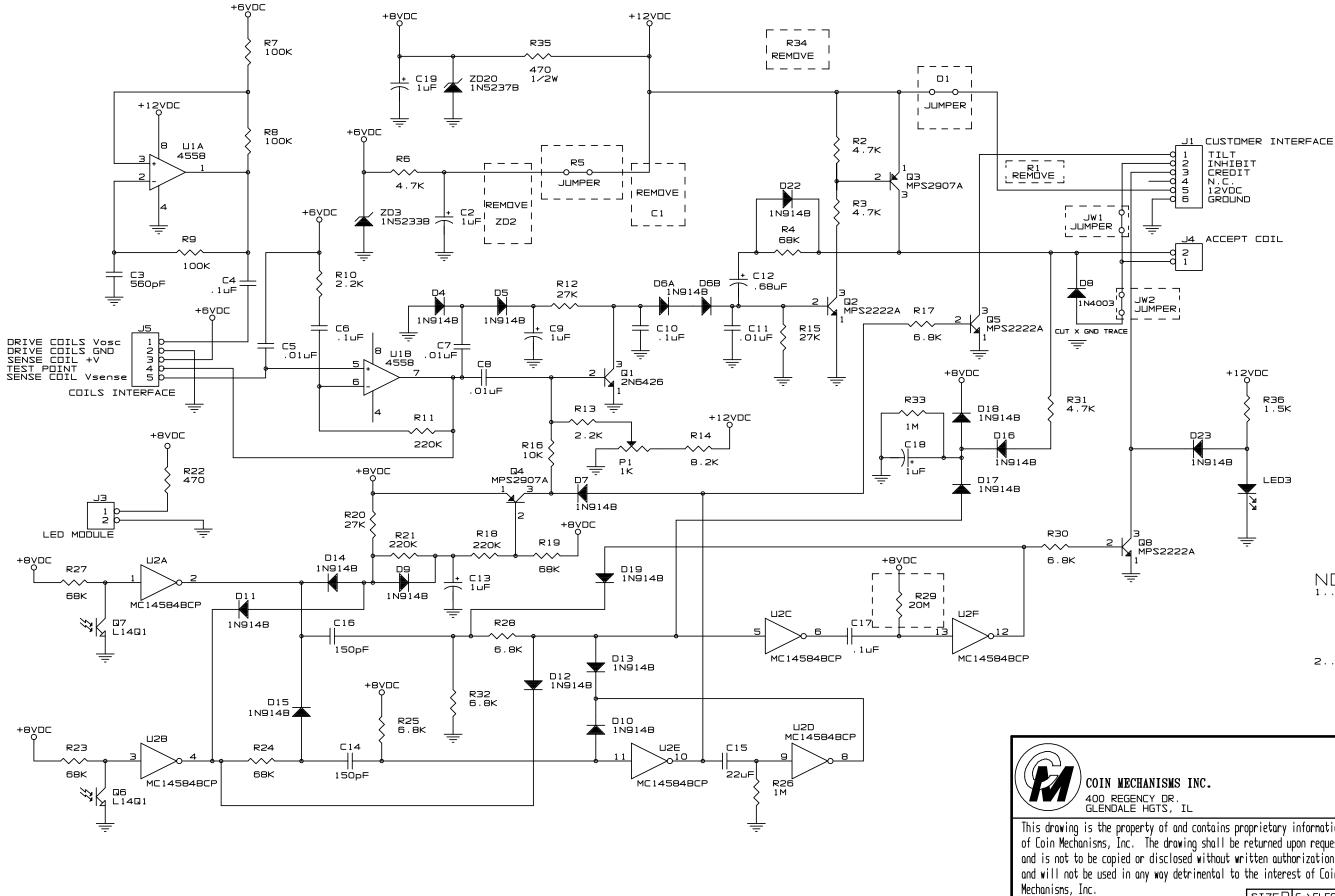


NO.	ECO NO.	REVISIONS	DATE⁄BY
1	6150	SEE SHEET 1	7/3/00 BPP

 REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM. ALSO SEE PARTS LIST FOR COMPONENT VALUES.
 MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN.. UNLESS SPECIFIED OTHERWISE
 MAXIMUM COMONENT LEAD PROJECTION TO BE .085 IN.
 SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
 ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

			TITLE PCB,CTRL,CC40,24VAC/DC				
IS INC.			C4	C48MS,T13S,GAIN 2			
IL			DRAWN	BPP	DATE	9/2/99	
and contains pro	oprietary	information	CHKD		DATE		
awing shall be r osed without wr	returned u itten outh	pon request prizotioo	APP		DATE		
detrimental to t	the intere	st of Coin	DWG NE	0001000		REV 1	
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF 1	

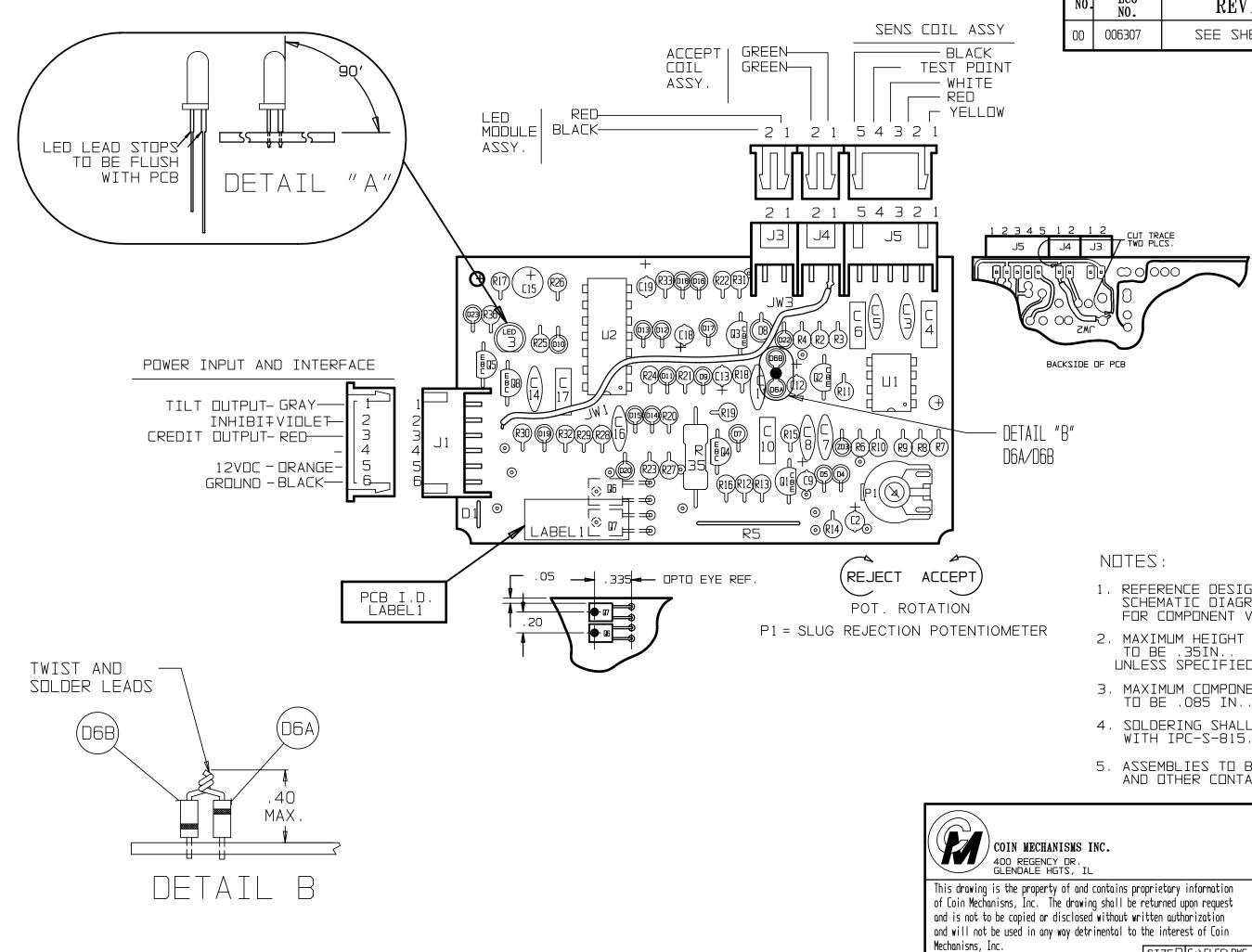
00



NO.	ECO NO.	REVISIONS	DATE⁄BY
00	006307	NEW RELEASE PCB GENERATED FROM VERSION 09270149 WITH R29 @20M	11/28/00 SJS

NDTES: 1...Unspecified capacitors are rated as follows: ...UF - 25V ...UF - 63V ...68uF - 35V 1uF - 35V 150F - 500V 2. TIT PU SE WIDTH - R25 \* C15 2...TILT PULSE WIDTH = R26 \* C15 CREDIT PULSE WIDTH = R29 \* C17

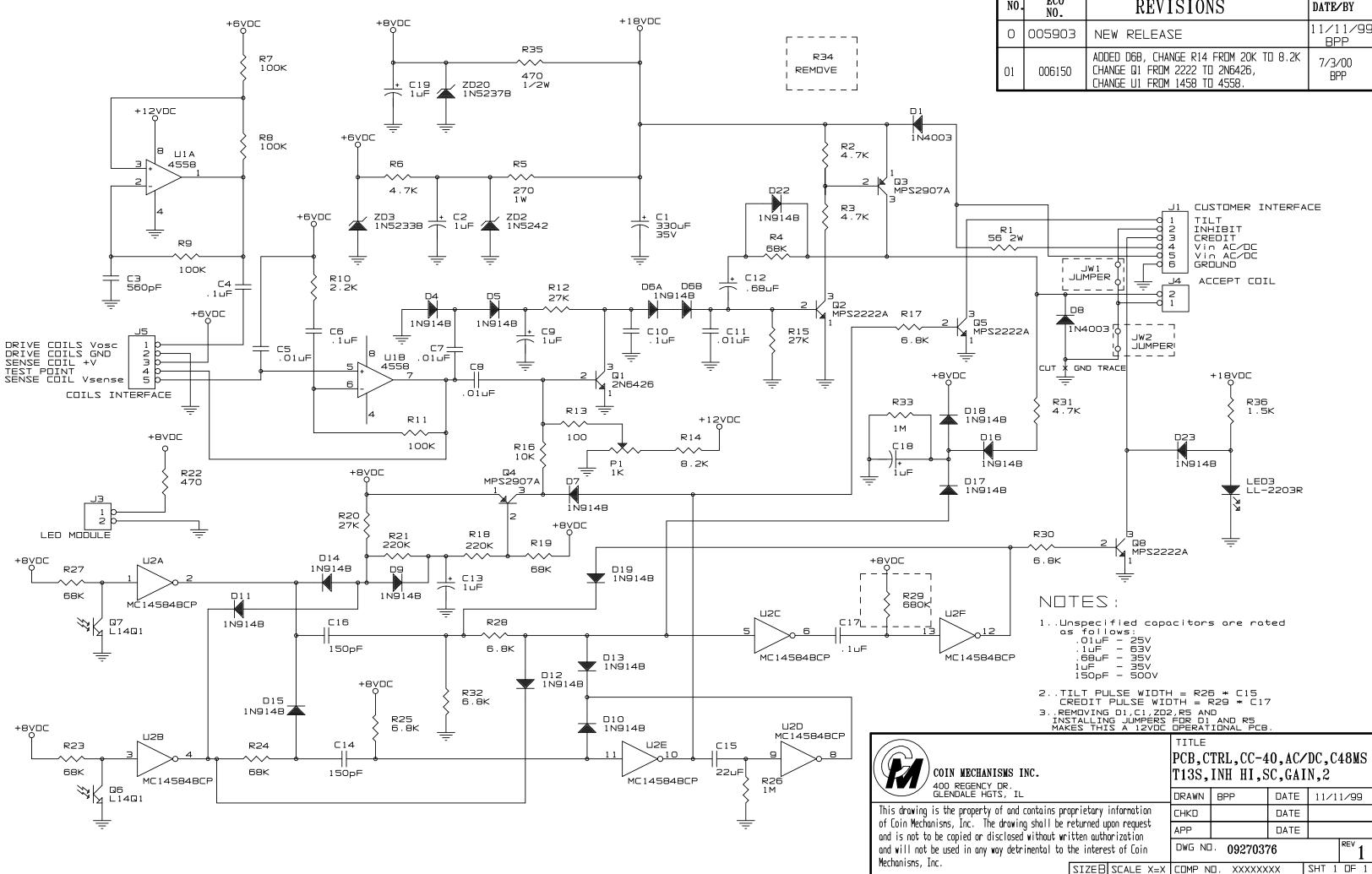
S INC.			TITLE PCB,CTRL,CC-40,12VDC,C1.5sec, T13s,INH,SC,GAIN 1				
ĪL			DRAWN	22	DATE	5/1/	92
and contains proprietary information awing shall be returned upon request osed without written authorization detrinental to the interest of Coin SIZEBG:\ELEC\DWG-F			СНКД		DATE		
			APP		DATE		
			DWG NO	0927036	35		REV 0
			REL\0927	09270365\36	5R1\	SHT 1	OF 1



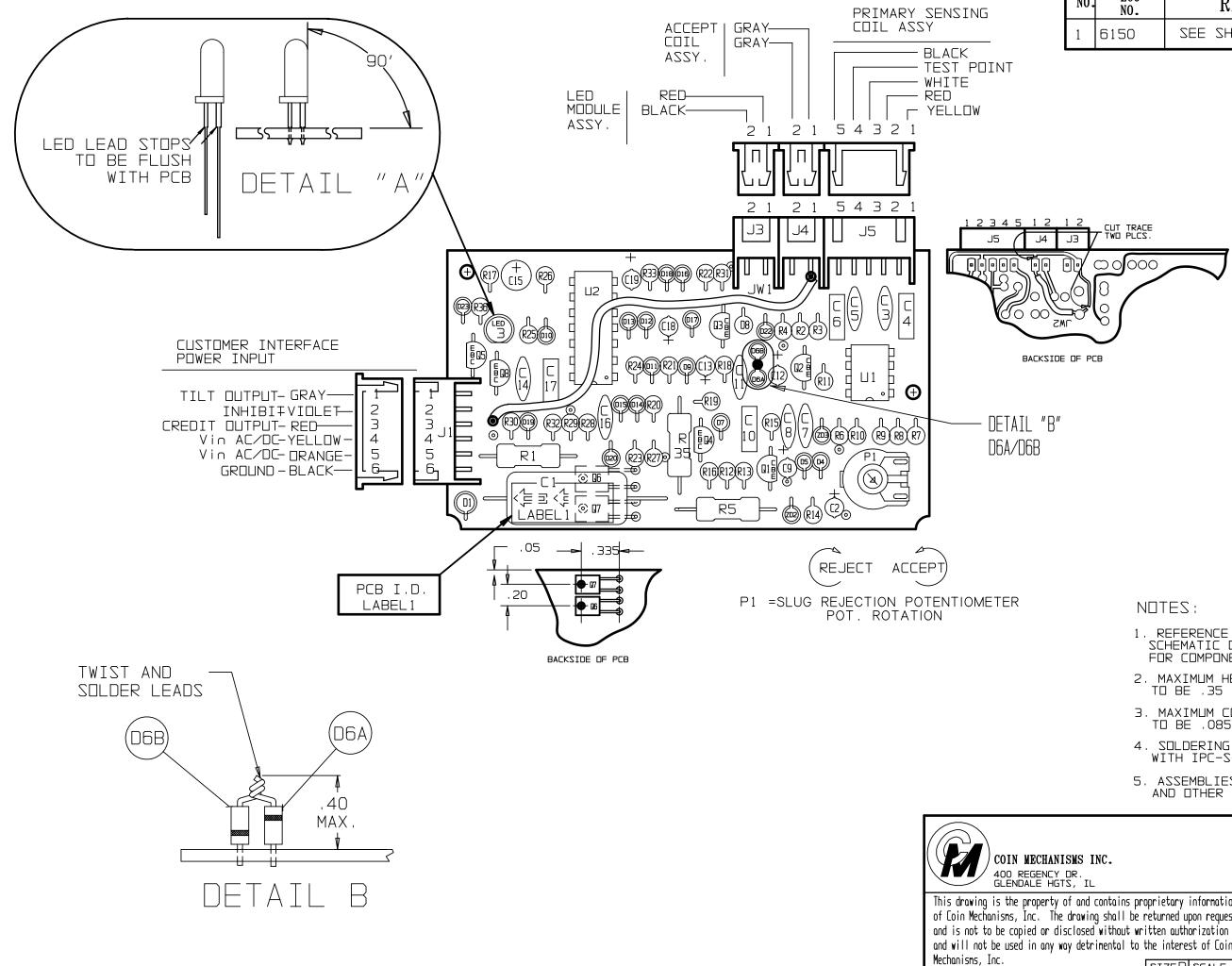
NO.	ECO NO.	REVISIONS	DATE⁄BY
00	006307	SEE SHEET 1	11/28/00 SJS

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM ALSO SEE PARTS LIST FOR COMPONENT VALUES
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35IN.. UNLESS SPECIFIED OTHERWISE.
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND OTHER CONTAMINANTS AFTER SOLDERING.

S INC.	TITLE PCB,CTRL,CC-40,12VDC,C1.5sec, T13s,INH,SC,GAIN 1				
ĨL	DRAWN	22	DATE	11/28/00	
and contains proprietary information	СНКД		DATE		
awing shall be returned upon request osed without written authorization	APP		DATE		
detrimental to the interest of Coin	DWG NO	0927036	35	REV 0	
SIZE B G: ELEC DWG-	REL\0927	09270365\36	5R1	SHT 1 DF 1	



NO.	ECO NO.	REVISIONS	DATE⁄BY
0	005903	NEW RELEASE	11/11/99 BPP
)1	006150	ADDED D6B, CHANGE R14 FROM 20K TO 8.2K CHANGE Q1 FROM 2222 TO 2N6426, CHANGE U1 FROM 1458 TO 4558.	7/3/00 BPP



NO.	ECO NO.	REVISIONS	DATE⁄BY
1	6150	SEE SHEET 1	7/3/00 BPP

- 1. REFERENCE DESIGNATIONS REFER TO SCHEMATIC DIAGRAM, ALSO SEE PARTS LIST FOR COMPONENT VALUES.
- 2. MAXIMUM HEIGHT ON ANY COMPONENT TO BE .35 IN..
- 3. MAXIMUM COMPONENT LEAD PROJECTION TO BE .085 IN..
- 4. SOLDERING SHALL BE IN ACCORDANCE WITH IPC-S-815.
- 5. ASSEMBLIES TO BE CLEANED OF FLUX AND DTHER CONTAMINANTS AFTER SOLDERING.

IS INC.			TITLE PCB,CTRL,CC-40,AC/DC,C48MS T13S,INH HI,SC,GAIN 2				
ÏL			DRAWN	BPP	DATE	11/11/9	99
and contains proprietary information awing shall be returned upon request osed without written authorization			СНКД		DATE		
			APP		DATE		
detrimental to			DWG NO	0927037	76	REV	1
	SIZEB	SCALE X=X	COMP N	D. XXXXXX	XX	SHT 1 DF	- 1